LD A,28 (C) Copyright 1983. L120: AND A Personal Software Services. SBC HL,DE Start at location 16514 with the L121: JR C.LI22 ZXGT header .The two HALTs cause the rest of the statement to be INC A JR L121 blanked out in BASIC. CP A ADD HL, DE 1122: JP L60 CP L 16 bit integer Negate a XOR H LD A,H CP C L231: CPL HALT LD H.A HALT LD A,L short section contains This CPL series of fixed jumping points LD L.A which are used to get from the INC HL first half to the second half of RET the code. Scroll the screen up 1 line L201: JP L151 ;PRINT LD HL,(16396) L131: JP L131 ;SCROLL L202: LD DE,33 JP L51 ;GET KEY JP L53 ;TEST KEY L203: PUSH HL L204: LD BC,0 1205: JP L165 ; PAUSE ADD HL,BC JP LIGO MULTIPLY L206: INC HL JP 0 ;PLÓT L207: PUSH HL JP L60 ;RST 10 JP L65 ;≇B6B L208: ADD HL, DE L209: EX DE,HL L210: JP L20 ;DIVIDE LD HL,726 L211: JP L99 ; RANDOM SBC HL,BC EX (SP),HL POP BC JP L80 ;GETNUM JP L187 ;REM L212: JP L187 ;REM JP L241 ;SAVE 1213: L214: EX DE,HL When the break key is pressed we LDIR come to here and do a RST to the POP HL ROM. LD BC,**≇02B**6 L252: RST 08 ADD HL,BC ADC A,H LD (16398),HL Routine to print a 16 bit signed LD A,33 which is held in the HL LD (±4039),A integer pair. A leading space register LĎ A,3 LD (±403A),A is added. RET a key to be PUSH HL L151: Wait for PUSH DE the value in return XOR A and test for break. Rub register CALL L60 trapped and waits out is BIT 7,H another key to be pressed. JR Z, L153 L51: CALL #288 LD A,22 INC H CALL L60 JR NZ,L51 CALL L231 CALL ±288 L57: LD DE,10000 1153: LD A,L CALL L160 CP #FF JR NC,L154 JR Z,L57 LD DE,1000 LD BC, ≢FD7F L54: CALL L160 XOR A JR NC, L155 PUSH HL LD DE,100 SBC HL,BC CALL L160 POP HL JR NC,L156 JP Z,L252 LD BC,≢FCEF LD E,10 CALL L160 XOR A JR NC,L157 PUSH HL JP L158 SBC HL,BC CALL L120 1154: POP HL LD DE,1000 L155: JR Z,L51 CALL L120 LD B,H L156: LD DE,100 INC H CALL L120 RET Z L157: LD E,10 LD C,L CALL L120 CALL ≢7BD L158: LD E,1 LD A, (HL) CALL L120 CP 0 XOR A RET POP DE See if any key is pressed, works like INKEY£ POP HL JP L60 CALL ±288 L53: L160: PUSH HL LD A,H AND A CP #FE SBC HL,DE JR Z, L51 POP HL JR L54 Pause for n 1/50ths ot a leaving if a key is pressed. 1165: SET 7,H RET Pause Repeatedly Subtract DE from HL 1 f until the result is negative. LD (16436),HL 28 A starts with character code the L166: LD HL,(16436) (=char 0) it finishes with character code of the left LD A.H hand AND ±7F of Print HL. this diait

7 X-8 This compiler by David Threlfall will speed up programs by as much as 60 times. 4 2 150 6

pressed.

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THE AIM OF this short series is to explain how it is possible to write a compiler for a machine as small as the ZX-81. We will look at the way that some of the Basic statements are translated - compiled - into machine code, and then go on to discuss extending the simple compiler to encompass string handling, multiple arrays and full variable names.

Why compiled Basic? Basic was written as an easy-to-learn language and to make it easy to run it was designed to be interpretative. This means that as each line of code is executed it is "retranslated" into a succession of calls to machine-code subroutines stored in the Basic ROM. This does not mean that Basic is translated into machine code - far from it — that is the purpose of a compiler. This method makes the language very easy to work with, particularly for developing a program, but slow to run.

Many compilers exist for the larger desk-top computers such as TRS-80, Pet, Apple and CP/M-based machines but those offering full floating-point arithmetic produce code only a few times faster than interpretative Basic as

character.



most of the time is spend executing maths routines in the ROM rather than interpreting the code. ZXGT is the first true compiler small enough to fit into the ZX-81. To provide the necessary speed for games as well as to keep the compiler small — it is only just over 2.3K bytes — it only runs integer Basic and so is restricted to numbers from -32768 to 32767with no strings or Boolean operations. The result is extremely fast code.

Before we discuss writing a compiler in detail it might be as well to define the purpose of a compiler. Essentially there are two main types of compiler, the first takes each line of Basic and converts it into a very compact set of simple instructions which are interpreted by a run-time system. The second type takes the lines of Basic and generates machine-code statements which carry out the functions of the Basic. Amongst these statements there may be calls to larger hunks of machine code within the operating system.

The first type of compiler produces very concise code but it cannot be executed directly by the microprocessor; instead, it is interpreted. The second type of compiler produces somewhat longer code which is directly executable and much faster.

When the design of this compiler was contemplated, experience of other compilers such as Microsoft and Accel led to the conclusion that "speed meant integer". To get the most speed the generation of an intermediate was rejected in favour of a mixture of pure machine code and calls to a library of fast routines within the compiler run-time system. It was expected that this would lead to an average speed up to about 20 times; in the event it was almost three times better than this.

The ZX-81 is a good target for a compiler for several reasons. First, its interpretative Basic is very slow. On standard benchmarks running in Slow mode it is 10 times slower than the BBC machine making it impossible to write good games in Basic. A compiler would increase the speed and integer arithmetic should be sufficient for most games.

Second, its Basic, in common with many (continued on page 135)

	RET Z
	CALL L53
	JR Z,L166
1ultinív	two 16 hit signed
interpry	tooothan One in HI
htegers	is DE Desult is U
che other	in DE. Result In ML.
L100:	LD B,16
	LD C,D
	LD A,E
	EX DE HL
	LD HL.0
1101.	SPL C
21017	PPA
	TR NC 1102
	ADD UL DE
	ADD HL, DE
L102:	EX DE, HL
	ADD HL, HL
	EX DE,HL
	DJNZ,L101
	RET
lot or	unplot a pixel on the
Creen.	Assumes a full 24 line
2 charac	ten schoon
ia.	
L0:	LD A, #28
	SUB B
	JP C,L253
	LD B,A
	LD A,1
	SRA B
	JR NC.L1
	LD A.4
11.	SPA C
-11	ID NO LO
	UK NU,LZ
	RLU A
L2:	PUSH AF
	CALL L15
	LD A,(HL)
	RLCA
	CP ±10
	JR NC 14
	RRC A
	JR NC 13
	YOP 405
1.0.	
L3:	LD B,A
L4:	LD DE, ±0C9E
	LD A,(±4030)
	SUBE
	JP M.L6
	POP AF
	CPI
	JR LZ
L6:	POP AF
	OR B
L7:	CP 8
	JR C,L8
	XOR #8F
L8:	JP L60
115:	LD A.23
	SUB B
	UF 1,1203
	AND #1F
	LD C,A
	PUSH BC
	PUSH BC
	PUSH BC
	XOR A
	RIB
	DI D
	RL D
	RL B
	RL B LD L,B
	RL B LD L,B LD H,A
	RL B LD L,B LD H,A ADD HL,HL
	RL B LD L,B LD H,A ADD HL,HL ADD HL,HL
	RL B LD L,B LD H,A ADD HL,HL ADD HL,HL POP BC
	RL B LD L,B LD H,A ADD HL,HL ADD HL,HL POP BC LD C.B
	RL B LD L,B LD H,A ADD HL,HL ADD HL,HL POP BC LD C,B LD B.A
	RL B LD L,B LD H,A ADD HL,HL ADD HL,HL POP BC LD C,B LD B,A ADD HL BC
	RL B LD L,B LD H,A ADD HL,HL ADD HL,HL POP BC LD C,B LD B,A ADD HL,BC POP BC
	RL B LD L,B LD H,A ADD HL,HL ADD HL,HL POP BC LD C,B LD B,A ADD HL,BC POP BC
	RL B LD L,B LD H,A ADD HL,HL POP BC LD C,B LD B,A ADD HL,BC POP BC LD B,A
	RL B LD L,B LD H,A ADD HL,HL POP BC LD C,B LD B,A ADD HL,BC POP BC LD B,A ADD HL,BC
	RL B LD L,B LD H,A ADD HL,HL POP BC LD C,B LD B,A ADD HL,BC POP BC LD B,A ADD HL,BC LD B,A ADD HL,BC LD BC,(16396)
	RL B LD L,B LD H,A ADD HL,HL ADD HL,HL POP BC LD C,B LD B,A ADD HL,BC POP BC LD B,A ADD HL,BC LD BC,(16396) ADD HL,BC
	RL B LD L,B LD H,A ADD HL,HL ADD HL,HL POP BC LD C,B LD B,A ADD HL,BC LD B,A ADD HL,BC LD BC,(16396) ADD HL,BC INC HL
	RL B LD L,B LD H,A ADD HL,HL POP BC LD C,B LD C,B LD B,A ADD HL,BC POP BC LD BC,(16396) ADD HL,BC INC HL LD (16398),HL
	RL B LD L,B LD H,A ADD HL,HL POP BC LD C,B LD C,B LD B,A ADD HL,BC POP BC LD B,A ADD HL,BC LD BC,(16396) ADD HL,BC INC HL LD (16398),HL POP BC

(continued from page 133)

other machines, is tokenised, this means that all reserved words such as Let, Print and so on are represented as a single byte of code. This makes them very easy for the compiler to recognise.

Its Basic is rigorous. You may not leave out Let as in many Basics and after Then the word Goto may not be omitted.

The Basic is syntax-checked on input, so much of the error checking is already done. In fact, the only "errors" which can now occur arise where a particular function - such as strings — is not supported by the compiler.

The decision to restrict the scope of the compiler was taken for several reasons. The first was that the compiler was written in assembler language rather than in machine code, and only about 1.5K's worth of machine code can be held in assembler in a 16K machine. The compiler was written in two roughly equal sections yielding a total of 2,300 bytes of machine code. Also, the ZX-81 has no discs and only a very slow tape interface, so the compiler has to be in memory at the same time as both the original Basic and the machine code generated by the compiler. So keeping it small was important.

The first restriction to be imposed was to limit single letter variables, A-Y. By having only 25 variables a predefined area of memory can be set aside for them which may be accessed by adding the base address of this area to the position of the letter in the alphabet. This position is fixed in the compiler and if a variable is required the code to load it into the accumulator can be written by the compiler.

Next, the compiler was limited to one array Z. By having only one array, which may be of any size that will fit in the machine, it can be placed above the Basic system and below the machine stack.

Furthermore, strings were omitted to keep the compiler small and to avoid the dynamic allocation of space their use entails. Dynamic allocation means that as the string gets longer more space is made for it; this can be slow.

For loops in this compiler have only positive single increments; the Step keyword is not supported. Also Boolean algebra - And, Or and Not - is not available.

Arithmetic expressions, except in Let statements, must be enclosed in parentheses as this makes the scope of the expression considerably easier to assess. This does not invalidate execution of the code in normal interpretative mode.

As regards the interface to Sinclair's Basic ROM, where possible the standard routines in the Sinclair ROM were used, but it soon became clear that in many cases this would not be possible. Two particular examples where it would at first seem possible to use the ROM routines but where it was decided to write special routines are worth considering.

Try plotting at every point on the screen and by careful observation you might notice that the top right-hand corner takes 70 percent longer to plot than the bottom left. Because of the variable length lines which are possible with the 1K RAM, the machine takes a long time to calculate where a particular point is in (continued on page 137)

(listing co	ntinued from page 133)	charac	ters are and BC is the
	LD A,24 SUB B	number Printir	of them there are.
	LD (16442),A	present	t cursor position.
	LD A,33 SUB C	L65:	LD A,B
	LD (16441),A		RET Z
Print	RET the character of token		LD A, (DE)
that i	is held in the A register.		INC DE
If we	are at the bottom of the		DEC BC
for a k	(ey to be pressed. If it	Expand	Tokens into the full
is the	COPY Key make a copy. If the SLOW key scroll up at	Keyword	ds adding blanks front and
two lin	es per second. If it is	L68:	CP 67
screen	and continue, Break		JR C,L69
returns	to BASIC. Any other key		RES 6,A
L60:	PUSH DE		JP C,Ĺ78
	PUSH HL PUSH BC	L69:	LD HL, ±111
	PUSH AF		LD B,Á
	LD A,(16442) CP 2		CP 33
	JR Z.L63		JR NC,L70
L73:	CP 118		CALL L60
	JR Z,L61	L70:	BIT 7,(HL)
	JR NC,L68		JR Z,L70
L78:	LD HL,(16398)	L71:	DJNZ,L70 LD A.(HL)
	INC HL		BIT 7,A
	LD (16398),HL LD A.(16441)		JR NZ,L72 CALL L60
	DEC A		INC HL
	LD (18441),A LD A,(HL)	L72:	AND ±3F
			CALL L60
L62:	LD A,(16442)		JP L73
	DEC A	An ove	rflow of some sort has
	INC HL	L253:	RST08
	LD (18398),HL LD A,33	Divide	ADC A,D
	LD (16441),A	by the	signed integer in DE,
L61:	LD HL,(16398)	result	is in HL.
L74:	LD A,(HL) CP 118	220,	OR D
	JR Z,L62		JR 2,L253 CALL 140
	JR L74		PUSH BC
L63:	LD HL, (16396)		LD A,H
	ADD HL,BC		RLCA
	LD A,143		JR C,L253 LD C,E
L66:	CALL L53		LD B,D
	JR Z,L66 CP 40		PUSH DE
	JR Z,L67		EX DE,HL
	CP 63 JR Z.L75	L21:	ADD HL, HL
	CP 41		EX DE,HL
	CALL 2,L78 CALL L131		LD A;C
147.	JR L79		SUB L LD A.B
L79:	POP AF		SBC A,H
1.44.	JR L73 POP BC		EX DE,HL JR NC,L21
2011	POP HL	1 22.	EX DE, HL
	POP DE RET	L35:	XOR A
L75:	CALL		LD A,H RRA
L76:	JR 166 LD HL,10000		LD H,A
L77:	DEC HL		LD A,L RRA
	LD A,L OR H		LD L,A
	JR NZ,L77		UK H JR Z.L23
Print a	REI a sequence of characters.		EX DÉ,HL
DE po	pints to where the		(listing continued on page 137)

(continued from page 135)

the Display File. Using the entry point given in Mr Logan's book on the ZX-81 ROM it was possible to make the plotting an average four times faster if called from machine code. By modifying the calculation of the Display File address, knowing that ZXGT will only run on a 16K machine, it can be made 35 times faster than Basic.

The second problem is that if a Print causes the screen to be full after a call to Sinclair's printing routine, an error 5 results. When using Basic pressing Continue allows the screen to clear and the program to go on. However, if a machine-code program causes this to happen it will not return to that machine-code program. For this reason new Screen-handling routines are built into ZXGT which do not call the ROM. The final version handles all keyword expansion and, when the screen is full, gives you several options.

You can Press C to continue as before; or press D/Slow and the screen will scroll up at about two lines per second while your finger is on the key. Alternatively press Z/Copy and a copy is made on to the ZX Printer. A fourth option is to press any other key than C, D, Z and Break and the screen scrolls at speed -

Figure 1. The column on the right shows how many times faster ZXGT is.

Test	ZXGT	ZX-81	
I. For-Next loop *	(secc	onas)	
1000	0.11	19	172
2. If loop * 1000	0.30	27.4	91
3. As 2 +			
$A = L/L^*L + 1-L$	2.6	65	25
4. A\$ 2 +			
A = L/2*3+4-5	5.0	63	13
5. As 4 + GoSub	5.0	79	16
6. As 5 + inner			
For loop	5.7	206	36
7. As 6 + Array			
assign	7.0	280	40
		^	
		AVe	erage 56

up to about 40 lines per second. The scroll is much faster than Sinclair's and does not result in a fragmented Display File which is hard to clear. Try a screen of Scrolls in Basic followed by CLS; it takes several seconds to clear.

The speed improvement of ZXGT over Sinclair Basic is spectacular, varying from 13 to 172 times faster than Basic in the same mode - Slow or Fast.

For example, a For loop of 1,000 steps in interpreted Basic takes 19 seconds in Slow mode while ZXGT takes a little over 0.1 seconds. To get a feel for how quick this is, if this is run in Fast mode under ZXGT it takes 0.03 seconds and, by comparison, a large mainframe computer running Fortran needs 0.001 seconds. In addition, the order of statements no longer matters for speed.

On the standard benchmarks the Slow mode comparisons between ZXGT and Basic were as shown in figure 1.

It is difficult to comprehend what a factor of 60 speed-up on a program means until you realise one hour becomes one minute.

Now for the Assembler listing: those who wish to assemble the ZXGT code given with these articles should note the following. All numbers starting \$ are hexadecimal. All RST commands are in hex but the dollar is not

(listing continued from page 135)
RR H
LD A,C
SBC A,H
JP M,L22 LD A,C
SUB Ĺ
LD C,A LD A,B
SBC A,H
EX (SP),HL
ADD HL,DE EX (SP).HL
JR L22
POP BC
BIT 7,B JP NZ 1231
RET
Find the sign of the division
dividend positive. Sign is in B.
L40: LD B,H LD A,H
RLA CLARI
EX DE,HL
LD A,H XOR B
LD B,A
BIT 7,H JP NZ,L231
RET
Generate a pseudo random integer number usino the standard
Sinclair Seed. The result is
always between 0 and 32768.
L99: LD DE,(16434) LD H.E
LD L, ≢FD
OR A
SBC HL,DE SBC A Ø
SBC HL,DE
SBC A,0 LD E,A
LD D,0
JR NC,+1
INC HL LD (16434) HI
RES 7,H
KEI Make a REM statement to contain
the code which we are
generating. This REM always contains two 118's at the start
to stop BASIC trying to list the
ROM in here and this routine
must be executed in FAST mode. The length of code arrives in
BC. On return HL is the address
18816 is the location of the
first character in the REM.
L187: INC BC INC BC
ADD HL,8C
LD B,H LD C.L
LD HL, 18816
UALL ±YA3 INC HL
LD A,118
LD (HL).0

given. The labels are L0 to L255. All comment lines should be omitted. This is one third of the code. You will require a Rem of length 2,303 bytes as the first line in the machine. Note that this means that

PEEK 16511 + 256 * PEEK 16512 = 2303 The remaining two-thirds of the code almost fill the machine by themselves, so this third must be assembled and then deleted.

The entry point to the complete compiler is

INC HL LD (HL),2 INC HL POP BC INC BC INC BC LD (HL),C INC HL LD (HL),B INC HL LD (HL),234 INC HL INC HL LD (HL),A INC HL LD (HL),A INC HL RET Get a signed integer from the Keyboard – used for INPUT and to ask user where the code should be put (see the second half of the code). The result is returned in the HL register pair L80: LD HL.0 PUSH HL L82: PUSH HL L89: CALL L51 CP 22 JR NZ,L87 POP HL PUSH AF PUSH HL CALL LAG JR L89 CP 118 POP HL 187: JR Z,L88 ADD HL,HL PUSH HL ADD HL,HL ADD HL,HL POP DE ADD HL,DE SUB 28 LD B,0 LD C,A ADD HL,BC ADD A,28 CALL L60 JR L82 1.88 POP AF CALL Z,L231 L92: RET Save the code away into the the location pointed to by location 16507. If the SLOW key is pressed display the location and contents - useful in debugging. L241: PUSH BC PUSH HL LD HL,(16507) PUSH AF LD (HL),A PUSH HL INC HL LD (16507),HL CALL L53 CP 41 JR NZ,L242 LD A,118 CALL L60 POP HL CALL L151 POP AF LD H,0 LD L,A CALL L151 JR L243 POP HL L242: POP AF POP L243: HL POP BC XOR Α RET

17389 - use Rand Usr 17389 - for the code to be put in a Rem at line two or 17381 if you wish it to ask where the code is to be put.

The entry to the code generated by ZXGT is at 18823.

Next month we shall give details of the way that the compiler translates Basic with examples from the standard Basic repertoire. We shall also supply a complete listing of the compiler in machine code.

Sindle in

David Threlfall continues his short series with the complete machine code for ZXGT, a true compiler for the ZX-81. ZXGT is only just over 2.3K bytes. The fast

code is the result of limiting the compiler to integer Basic. This month, details are given of the way that the compiler translates Basic with examples from the standard Basic repertoire.

GT COMPLER

LAST MONTH we considered the philosophy behind ZXGT, my ZX-81 Basic compiler. This month we move on to the intricacies of integer arithmetic evaluation and see how some statements compile.

ZX-8

X81

E 20 30 10 50 60 10 80

E a la al al rai US

For those uninitiated to Z-80 machine code here are a few preliminaries. ZXGT uses the Z-80 registers A,B,C,D,E,H and L. A is the eight-bit accumulator. H and L may be considered as a single 16-bit accumulator. All the registers may be used for eight-bit storage but the pairs BC and DE may also be used in 16-bit manipulations. Putting a register pair or 16-bit number in brackets means that the value in brackets should be taken to point to the location required. For example:

LD HL,n load HL with the value n

but LD A,(HL)

(HL) means load register A with the data in the location pointed to by



the value in the HL register pair. Here are a few simple examples to start the description of the compiler.

CLS This results in a call to Sinclair's ROM at hexadecimal address 0A2A. RETURN

This one is very easy, requiring the Z-80 instruction Ret – return.

PEEK n This causes HL – the double-precision

accumulato	r — to be loaded with the contents
of location	n, thus:
LD HL,n	load HL with n
	load the accumulator with the

	contents of location HL
LD L,A	move A into L
DHO	zero H

HL now contains the contents of location n.

The next example is:

ABS X

Load HL with X and test the top bit of H - the sign bit. Call a negate routine if this bit is set, that is, if the number is negative. Negating a number entails taking the 2's complement but there is no Z-80 instruction for this. Instead we must take the 1's complement of H and L independently and then increment HL.

POKE x,y

This means put the lower byte of y in location x. As we need x and y simultaneously they cannot both be in the HL register pair. Therefore we get x in HL and y in DE then do LD (HL),E as required remembering that Poke acts on only one byte.

GOTO n

This will be translated as a Jump - JP - instruction; n must be a number and not a variable. The compiler has two passes. On the first, it generates a table of line numbers and their addresses in the machine code. On the second pass, the correct addresses will be available for both forward and backward Gotos. (continued on page 79)

(continued from page 77)

Note in particular that, in the compiled code, the nearness of n to the start of the program does not affect the time taken to execute the Goto.

GOSUB n

This works just like a Goto except that it results in a Call instead of a JP. USR x

This results in the machine code at location x being executed. It looks as if it should result in just a Call to location x. However, there is no machine code statement for "call to the address given by a register pair" that is, CALL (HL)

so subterfuge is necessary. Consider the following code:

LD BC, BACK load BC with the address of label BACK. PUSH BC keep BC on the stack LD HL, x get x into HL PUSH HL and push HL on to the stack

BACK: continue code

The first four lines get the address of Back and the address to which we wish to go on to the stack. The Ret instruction makes the machine "return" to the address at the top of the stack which is x, just as we wanted. At the end of the routine starting at x, a Return causes a jump to the next address on the stack which is Back and there we are. FOR-NEXT

The For-Next pair is compiled into directly executable code — not calls to other routines — and so a For-Next loop is extremely fast. The For statement has the form:

FOR K = M TO N

where M and N may be parenthesised expressions. M is moved into variable K and (N + 1)is stored in the next word/two-byte location. During compilation the address of the next location after For — let us call it zzzz — is also stored. The next K statement is compiled thus:

LD HL,(nnnn)	where nnnn is the location
	where variable K is stored.
INC HL	make K one bigger
LD (nnnn),HL	store this value
LD DE, $(nnn + 2)$	get value of end of loop
AND A	clear carry flag
SBC HL,DE	subtract DE from HL
JP M,zzzz	if HL-DE is negative jump to
	the next address after FOR.
	Otherwise execute the next
	instruction.

This arrangement results in extremely fast execution of the final code — about 170 times faster than Basic. Machine-code enthusiasts might care to consider what limitations the test places on the values of M and N.

Now for some arithmetic. Wherever a variable may be used in Basic an expression may be substituted, so some means has to be found to evaluate that expression. The method which has been chosen for ZXGT uses an often-mentioned but rarely-used mathematical function called recursion.

For those who have not come across recursion before, consider evaluation of n!, that is, n factorial; n! is defined as:

n! = $n \times (n-1) \times (n-2) \dots \times 1$ and we may rewrite this as: n! = $n \times (n-1)!$

= n \times (n-1) \times (n-2) ! etc.

To calculate n! it is necessary to multiply n by (n-1)!. To calculate (n-1)! we multiply (n-1) by (n-2)! This process is continued until we arrive at 1! which is 1. Figure 1 shows a flow diagram for this process. The routine Factorial calls itself repeatedly.

The process of evaluating an expression uses a similar technique which is shown in figures 2 and 3. We see that Variable calls Evaluate and Evaluate calls Variable, but the way out may not be clear. Each time we enter Evaluate, a marker -0 — is pushed on to the compiler stack and when we reach the end of the line or a right parenthesis, the stack is popped back to see what "pending operations" are left.

Operations are performed until an "operator" 0 is encountered. We placed this (continued on page 81)



(continued from page 79)

there to mark the end of the expression when we entered the routine. The exit is taken with the result in HL.

Two other points should be noted. Firstly, the right and left parentheses will match



exactly, because of Sinclair's syntax checking.

Secondly, we are using two different stacks.

The operands of the expression are pushed

on to the stack of the compiled program by

code generated in the compiler. The operators

Basic may be run and tested under the inter-

preter before the compiler is invoked. One

writes a program bearing in mind that

eventually it will be compiled. When you are

satisfied with your code a single Usr command

(continued from page 81)

POKE 16514,118 POKE 16515,118 POKE 16510,0

You now have a Rem called line zero of length 2,303 bytes as required. Note that there are minor differences between the assembler code and the version of ZXGT in the hex dump.

Figure 4 shows the hex loader that will enable you to enter ZXGT. Note that the Rem to contain the code must be exactly 2,303 bytes in length, that is the total line length as defined by Sinclair. The loader will prompt

Figure 5. Hex dump of ZXGT compiler.

16514 16524 16534	18F8DAC897676C38440C3=1607 22341C35741C34F41C385=1114 341C39741C3A841C32142=1201
16544	4C39C42C3DE42C33943C3=1414
16564	6E5D5AFCD2142CB7C2808=1296
16574	73E16CD2142CD18411110=718 827CD0741301R11E803CD=847
16594	907413015116400CD0741=535 030131508CD07413012C3=645
16614	1FC40CD0D4111E803CD0D=1069
16624 16634	241116400CD0D411E0ACD=710 30D411E01CD0D41AFD1E1=1001
16644	4C32142E5A7ED52E1C93E=1497
16664	6C321427C2F677D2F6F23=886
16674	7C92A0C40112100E50100=599 8000923E519EB21D602ED=1019
16694	942E3C1EBEDB0E101B602=1544
16714	103323840C9CDBB027CFE=1148
16724	2FE200ECDBB022420FACD=1217 3BB027DFEFF28F8017FFD=1492
16744	4AFE5ED42E1CAB24001EF=1616
16764	6C84DCDBD077EFE00C9CB=1462
16774 16784	7FC2234402R34407CE67F=1041 8B5C8CD4F4128F306104R=1109
16794	978EB210000CB391F3001=731
16814	1DADC42473E01CB283002=931
16824 16834	23E04CB293002CB07F5CD=1020 3EB417ECB07FE103007CB=1164
16844	40F3002EE8F47119E0C3A≠762
16864	602F1B0FE083802EE8F18=1144
16874 16884	7363E1790DADC4279E61F=1169 84FC5C5C5AFCB10CB10CB=1486
16894	910686729290148470901=843 047095548004009232205=560
16914	140C13E1890323A403E21=754
16924	291323940C9D5E5C5F53H=1459 33A40FE02283AF1FE7628=1129
16944	428FE403072280E407723=796 5220E4038394030323940=523
16964	67EFE76204A3A3A403D32=895
16974	7384023220E403E213239=471 84018382A0E407EFE7628=802
16994	9E62318F82R0C4001F802=906 009368FCD4F4128F8FF28=1140
17014	12815FE3F281BFE292008=780
17024	2211027287D8420F8CD23=959 3411803CD2A0AF1189AC1=961
17044	4E1D1C9CD690818D378B1=1485
17064	6433809FEC0CBB7DA3542=1301
17074	7E63F2111014704FE2130=754 804AFCD2142CB7E2328FB=1138
17094	910F97ECB7F2006CD2142=1063 02318F5E63ECD21428EC3=1271
17114	12D42CF8A7BB228FACD29=1293
17124	243C57CB20738F14B4211=1028 30000D5EB2329EB297995=1070
17144	4789CEB30F6EBEBAF7C1F=1605
17164	61CCB1D7995789CFAFE42=1376
17174	779954F789C47E319E318≏1199 *8DDE1C1CB78C21B41C944=1517
17194	97C17DC1B41EB7CR847CB=1260
17204	07CC21B41C9ED5B324063=1152 12EED78B70600ED5298ED=1318
17224	252985F50ED5230012322=846
17234	3324408800903030303038=915 4030303218049CDR30923=655
17254	53E767123360223C17123=760

with the address to be loaded and you should refer to figure 5 to see the hex string that should be entered.

You should input the 21 characters up to, but not including, the equals sign followed by Newline. You will then be prompted for the check number by an equals sign. If the hex and decimal agree you will be prompted for the next line of input. If they disagree you will be asked to re-enter the data. The last hex string only contains five characters.

The entry point to the complete compiler is 17389 - use Rand Usr 17389 or Let L=Usr 17389 - for the code to be put in a Rem at

line 2 or 17381 if you wish it to ask where the code is to be put.

The entry to the code generated by ZXGT is at 18823. Use

Let L = USR 18823

Do not use RAND USR 18823

In future issues, we shall give the remaining third of the ZXGT assembler code and discuss how to avoid some of the restrictions mentioned in part 1. If you find the listing too daunting to type in the compiler is available on cassette for £8.95 from Personal Software Services, 452 Stoney Stanton Road, Coventry.

	17064	C70000CE00077007700C0-070	10044	410C4E5C5191CEE192010=1049
er.	17259	5/02335En23//23//23U3+3/3	10044	41004F3031010FE102010-1040
	17274	7210000ESESC05741EE16=1124	18054	5FF17C2C144CDC6462162=1336
	11214	121000000000000000000000000000000000000		C1000004710000000C4C01-1000
	17284	82009E1D1F5E5CD214218=1277	18064	649CDH94718MCCDC646Z1=1220
607	12004	OF OF FT OF TOOL FOOT FOOT SOON 1960	10074	766401060065100784961±1079
77.4	17294	JFUFE/6E1281F23E32323=1200	10014	(0E4310F30EE1C0/D43F1-1313
114	17004	001104ECD214279D61C28=1036	19084	ВЕЕЙИС8ЕЕ152811CDC646=1259
201	11.204	001134FC0214273001030-1030	10004	
201	17314	1084F060009FF0838D3F1=874	18094	921H7EBCDHE4721ED52CD=1442
414	11014		10101	00F4710F70D0C460F100D-1965
747	17324	23EE6CD214218C7F1CU18=1291	18104	0HE4/18E/CDC6463E19CD=1263
331	17001	044000FF5007D40F577F5-1514	10114	17040100020100704955±1257
	17334	34109056528784065776571014	18114	1/6491000050100/64965~100/
296	17044	400000004000404100000+900	10104	2022000000404721756500±1086
10	11.244	4232270400041411623204332	10124	200200100404121100100-1000
10	17254	5123F76CD2142F1CDB440=1176	18134	38E47212600C38E47FED4=1222
47	11304	J12321000214221000440-1110	10101	4004000 40 4704 FD 460000-110F
141	17364	6F126006FCDB4401802E1=1090	18144	420120040472168460383=1100
35		774740407000000000000070707-1700	10154	E4701E1460EEEC06060C0C0-1410
	17374	7F1E101HF0909093E0F07=1723	18104	04/01F140C0E0020202-1412
45	17004	0007540101001001000000012=692	10164	6FEC4020F470D4445FF41=1391
069	11004	000/0491012210000001E-000	10104	
065	17394	944CDF74803CD7849E5CD=1427	18174	7C2C144215C49CDH9473E=1160
10	11004		10101	000010000047004000-1101
10	17404	00649E1CD1244CD5949CD=1167	18184	82621006FU3MB47FE40U2F1131
001		4004400004400700000-1170	10104	01047017040000947EED2±1216
407	17414	120440000440F7F360303=1170	10154	51H4721724505H2411E02-1210
497	17424	27040227040ED5R104012e845	18204	/////////////////////////////////////
21	11969	210432210402030104010-040	10201	
.91	17434	32172 40CD094528 704036=766	18214	14/3EU4217H47U3H847FE#1246
86	11404		10004	000000144004047004047-1059
~~	17444	418233668232279401168=392	10224	208020144034047004047413330
i99	1 7 4 5 4	500100070400170400004-00E	10004	2004445F53FF500784900±1484
210	17404	000135518405110400004-000	10404	00044401 0000000104000-1104
019	17454	6440000EE00001640CD44=795	18244	44D473ED1CD7B49E1C9CD=1467
544	11404	04423236363626104000444150		
944	17474	745FFF2000B44FEF900096=1897	18254	04440FEI6F0UU4440FEI0F1262
49			10004	20012554000055020400~1075
140	17484	848FEFHCCC848FEDE28EH=1802	10204	02010F6400000FE200402410(0
148	17404	OFFECCTONOFFECCCODAS-1500	19274	747DC8F47F1CC7547C9CD=1544
217	11494	JFEEJUUIU4JFEEUUU204JF1099	10214	
C 1 1	17504	0FFFDCC4045FFFFBCCC242=1291	18284	8CB4618F6CD3D4618F121=1177
492	11004			070474005700567700565-004
	17514	1FFF3CCF447FEF4CC0049=1775	18294	9/84/182F(U2F6//U2F6F=621
616			10004	0220900F145FF64092F49=1442
E40	17524	2FEF4008647FEE300F148=1955	10304	020020001400204000074241440
043	17504	OFFEEDORNAOFEFEODEDA0-1745	12214	13E28C38B47CD28483E21=955
462	11034	SPEEELUSUMOPEPOUDUMQ-1/43	10014	- A second second second second second second second second second second second second second second second second second second second se
704	17544	4FFFCCC9248FFF10C0346=1700	18324	21815CD0D44E1ED487840=1055
Ø41	11.044			000010000100501501010-501
w T 4	17554	SFEFBCC9F47FEE3CR9647=1843	18334	007212M0M100021064718P031
109	1.001		10044	4003EC0C07E4970C07E49=1194
-	17564	6FEF3003343FEEM2813FE=1660	10344	700000000000070771124
51	17574	757000447555500004455±1717	18354	570037B490D37470D0147=1315
247	11014	I EL COMPATICE E COUCOMACE ATTAN	10004	
6.7I	17584	800200ECD4445FE762007=799	18364	621EB7318EDFE18C2C144=1379
31	11004			2000DAAASODE1ASESSOD-1201
ā.	17594	9E1CD0E49C236443EB8CD=1284	18374	70700444000F140E0E000F1721
020	17004	020400050400000004042-1050	10004	844450D37472E230D7849=966
164	17664	00043000343053600404771203	10004	0777000017106200010727200
104	17614	1215F49C30947F556235F=1090	18394	921ED53CD8E47E1CD8E47=1478
62	17014	121064200002160005006-1000	10024	
0 <u>6</u>	176.24	2D5EBE5222340220A40CD=1123	18404	03E222323CDHB47E1ED5B=1166
271	1000		10114	170400000450000444500-1010
	17634	3EC45CD5349E1CD13451B=1211	18414	17840000340030044400071218
144	17/1	410000100004800507040-1000	10404	251455555252626Chap4721-1252
100	17,544	416EBU1000040EV08/64051260	10424	5L14060600065U004151-1005
102	17651	5000945F5F058724097F0±1422	19494	32322CD8F47F1CD8F4723=1229
486	11004	00000740E0E000124001E0-1422	10404	
400	17664	652E13803227240E10973=1119	18444	4233EEDCD7B493E5BCDAB=1264
43	11004	002010000201040010010-1110		
	17674	7237223C95E235623C9E5=1065	18454	54/3EH/21ED52CDH84/3E=1161
60		0010010400000001455007-1050	10464	ACOCICNOCASEDCDODA7CO-1646
E 4	17684	8012H104023000E43E8H(=1032	18464	6FHE1000E40E000H04703-1040
54	17694	05040000000000000000000000000000000000	19474	7210000180FCD4445FF7F=794
450	11024	260420020124060020060-1001	104/4	121000010010044401616407
402	17704	02318FD3FC3F5CD4445CD=1345	18484	82818F5110800CD6249F1≠955
129	11104		10101	Inched ALERIANOULA A-LEOA
	17714	12H48CD1345EBCD0E45E B =1165	18494	9FE26020144FE10080144=1024
96		054000047050010505557-1660	10504	ANCI COCOONEGO ONEGEO OSA
20	1//24	2F1U3M8473EUU18E9E0E7#1662	18004	0061006004F09180FE32H=834
23	17704	200722000000702005550De1296	10514	116401105001922164051=478
98) (198)	1((34	3FE(62603FE(E2603F 300 - 1230	10014	1164011000019221640E14470
20	17744	46849F1F1C9CD4445FF76=1558	18524	2092175490D89470D4445=1211
71	711144	40040FIE100004440FE10-1000	10024	
1.4	17754	52877FE0B2813FEC1284C=1046	18534	3CDF145C338463E9BF5CD=1503
62		CEED.CO07400E04701E040-1100	10014	4074700014701404E000E-14E1
00	17764	6rEU62874000047210349=1169	18544	43747UUU147214843UUHE=1131
96 	1	700004710452510007040-1005		
140	11((4	(UDU24(104F3C10UD(04741033	18554	5473E3ECD7B49F1CD7B49=1238
140	17794	8287840F5CD784901FFFF=1870		
80	11104	0211040200704901111-1010	18564	63E32213040CDHB472165=838
	17794	928164023037EF5CD6849=919	10874	749029947259910005299-1024
59			180/4	(=20302=(300010003020=10/4
C 1	17804	0F1FE082800CD784918EF=1215	12524	8213440CD6B473E222132=775
10 I	17014	1001640E10571000E1100=974	10004	
495	11814	1441040010011400001100-974	18594	940C38847FEDD2812FE14=1307
-00	17824	288473E01E1CD88472168=1117	1000	000141E0001ED1707EDE0-004
083	11044		18604	020141F3001E61/M/EU32=884
004	17834	349CDR9471812CD3747CD=1096	19614	137F817D01808FD5237C0=1132
301	1701	4014701404000007470105-1140	10014	15,, 51, 55, 55, 55, 55, 55, 55, 55, 55,
S.A.	17844	4014/214340C0ME4/21F3=1169	18624	21804ED5237C83FC9CD37=1126
04	17954	508006947004445FF1928±1115		047/70F0F0F0F47010/40-1000
138	11004	0000010410044401E1120-1110	18634	347672E3EUDHE4721H648=1003
100	17864	68CFE19288821EC45CD89=1307	10644	400894728084022001245-000
063	11004		19044	TUUNDAL TUUNA07000104040003
0.71	17874	747281640282216408FC9=738	18654	5EBCDØE45EB3ED2CD8B47=1477
271	11014	1 41 EN1040EDEE104000 03-130	10004	
292	17884	8CD4D473E7DCD7B492168=1078	18664	6CD4445FEDEC2C144C921=1507
c.20	17001	040000047100605760360-1005	10674	7574000000470057000100-1004
028	17894	949CDH94718D63E76C368=1235	18674	7F748U3H947UDE702213B=1284
	17004	049EE4002014406261717=1160	10204	840CBB6C9210640C20947=1197
070	11304	04315400201440020111141100	10004	0400000022100420007247-1127
	17914	1FD48794026006F09C9CD=1061	18694	9213840C8F6C307024F23≈922
605	11214		10004	
007	17924	24445CDF145FE64200ACD=1253	18704	04609233 R0C409 DC 03R0D=66 8
221	17001	000400EEE0070400CEEEE-1000	40744	140000000444855050004-1463
376	17934	32H493EE3CD784926FFE3#1329	18714	14090090D4445FE3F0201≉1467
310	17044	4004445EE140201440020-1227	10704	244004047950900404794-1199
199	17344	46044437614626144603051337	10/24	244004041050040412141183
	17954	546CDD345F17CFFFF200C=1457	18724	3ED48CD8E47211040008E=1266
517	11204	340000340E110FEFF2000-1407	101.34	
	17964	621D1EBCD8E47210945C3=1233	18744	447212909C3RE47CD2A49=914
260		70047050000047055500-4000	10764	FORDERD ZD40010F4F0D00-110C
-00	17974	/H94/3E22C3HB47HFF5CD=1398	18754	00ED0CD7649210E40CDH9=1166
152	17004	040472EEEC07040004445-1102	19764	64721FBD1039F47039840=1292
	17984	040473EE3U07643U04443=1182	10/04	041210001000040-1000
318	17994	9FF112850FF762840F500=1229	19774	7038B40038F4003914003≈1398
	11224	5, C112000, C102040F0000-1323		
40	18004	04D47F1FE152804FE1620=1016	18784	83440C39740C39R40C39D=1387
1 5	10001		10704	04000000000000000000000000000000000000
1 J	18014	123016778FE002004F5E5=1215	18794	240U3N040EBU3N340U3N6=1301
	10004	21000ES214440CD0E47CD-1002	10004	040030940030040030540±1257
50	16024	21008E021444000HE4700=1302	10004	9499907489900098690000486713 <u>01</u>
6 0	10004	275462555216960000847=1160	19814	18F40=239
50	18034	3/E463EE3216768UUH84/=1168	10014	100 40-202

David Threlfall continues his insight into how the ZXGT compiler works and what must be considered in the design. Here he looks at the compiler's shortcomings and how they have been overcome.

Display File Spare				
ZXGT m/c ZXGT Assembler Code	ZXAS	Spare		
+ + 16509 18816	ф 27К	32К	-	

Figure 1a: Memory map during work on ZXGT



work on MCoder II

A FEW MONTHS after ZXGT had been put on the market as MCoder a few of its shortcomings became apparent. As it stood it worked extremely well, was very fast and far exceeded its original specification, but there are several areas where improvements would be welcome. Problem areas include;

■ No strings — this can be circumvented but string handling would make more a much more versatile compiler.

■ Only 25 variables — programs written for ZXGT were getting larger and this restriction was proving a major handicap to writing understandable code. Multi-character names were necessary, not even two-character names as on the Pet could be considered.

■ Only one numeric array — the array was added as an afterthought primarily to help fulfil the role of data logger on scientific experiments. A full 26 arrays, even if one dimensional, would be very useful.



■ Printing was not à la Basic — each number was printed with a leading space and "," did not tabulate.

Lprint was not supported.

■ No Boolean algebra at all — a little would be useful.

■ Input was totally non-standard, it appears on the main screen not on the input area and there is no rubout. Input at the bottom of the screen with the ability to correct mistakes was necessary.

• At about 56 times faster than Basic it was not slow but work on the Spectrum version had shown that more speed was possible — the final version is almost 70 times faster than Basic.

■ Brackets are necessary except in Let — this is compatible with Basic but annoying.

■ Last but still relevant was size. The original ZXGT had been aimed at 2K bytes and only just ran over but most users gave the

impression that to obtain the items above they would sacrifice a little more space and longer loading time. 4K was felt to be optimum if all these features could be added.

■ Floating point. Slight consideration was given to full floating-point arithmetic. However, work with Microsoft's compiler on a larger machine had shown that a speed up of only five or so could be expected particularly as Sinclair's ROM routines are very slow. Put another way the balance between interpretation and evaluation is heavily on evaluating. For this reason primarily it was decided to stay with integer arithmetic on all the compilers for Sinclair computers.

Readers may wonder why these features were not implemented in ZXGT. ZXGT was not written directly in machine code but in assembler, a listing of which has been appearing in parallel with this text. It is hard (continued on next page)

The	Sinclair functions are	INT ?	LD L,A	FOR statement seen. Generate
exami	ned here and code	1 201: CP 207	INC HL	code to load the variable with
30000	nriate to each is	JP NZ . L252	RET	the first number and keep the
OPDEC	ated.	JP L108	11	second number incremented by one
PEEK	2	1244: CALL L108	Cat unsights then code is	in the next location.
116:	EF 211	Get a nair of arouments and	constant for 1d bl. (pppp).	L100: CALL L240
	JE N7.1161	cenerate the code	contracted for rd nijeminist	CALL L37
	CALL 1188		L243: CALL L37	PUSH HL
			TD 7 1004	PUSH HL
		push hi ld bl sees as ld bl (pppp)	JP 2,L234	CALL L248
	LD HL #0026	to nijnana or to nijvananz	LU M, 42	CALL L244
	JP 136	pop de	Get integer then rode generated	LD A,35
USR ?	0. 200	CALL L240	for 1d bl. nonn.	CALL L241
L161	: CP 212	L246: PUSH AF		LD HL.#53ED
	JR NZ . L1 52	LD A,229	10 4.33	CALL L36
	CALL L108	CALL L241	IP 38	POP HL
	LD HL.L163	CALL LIVE	STOP statement seen	CALL 136
	JP L39	LD A,209	L118: CALL L96	LD A.34
L163	: LD BC.L160	CALL L241	POP HL	INC HL
	PUSH BC	PUP AF	LD BC.(16507)	INC HL
	PUSH HL	KEI	RET	CALL L38
	RET	Work out what the next thing is	CLS statement seen. Generate a	POP HL
L160	: LD H.B	is it a variable, a number of a	call to the ROM.	LD DE,(16507)
	LD L.C	function?	L52: LD HL, #A2A	CALL L170
	RET	L108: CALL L240	JR L39	RET
CODE	INKEY# ?		SCROLL statement seen. Generate	NEXT statement. Find the
L162	: CP 196		a call to the scroll routine.	variable name and generate code
	JP NZ,L168	CP 14	L130: LD HL,L131	to increment it and test if we
	CALL L240	TP 7 1 218	JR L39	have reached the end. See Text.
	CP 65	CP 44	A routine to save either HL as	L101: CALL L240
	JP NZ,L252		code, HL and A as code or to	CALL L37
	LD HL,L53	CP 38	generate a call to the number	PUSH HL
	CALL L39	CALL NC.L245	presently in HL.	PUSH HL
	LD A,38	CALL C.L86	L39: LD A,205	LD A,42
	LD HL, ±6F00	L98: POP AF	L38: CALL L241	CALL L38
	JP L38	CALL Z.L230	L36: LD A,L	LD HL, #2223
RND		RET	CALL L241	CALL L36
L168	3: UP 64	L97: CALL L16	LD A,H	POP HL
	JP N2,L172	JR L98	JP L241	CALL L36
		L218: CALL L200	PUKE, get the two arguments and	INC HL
ABS 2	JF L37	JR L98	make sure that a comma separates	INC HL
1172	CB 218	Generate negation code.	them.	LD A,237
L1/2	TP N7 1 201	L230; LD HL,L231		LALL L241
	CALL 1198	JR L39		
		Actually do the negation.	IP 1 34	
		L231: LD A.H	Check that the char in A is a	LD HI 4535D
		CPI	comma, if not error.	LU HL, EJZEU
	LD HL 1231	LD H.A	L107: CP 26	
	JP L38	LD A,L	JP NZ, L252	(listing continued on next page)
	10	CPL	RET 12	-

(listing continued from previous page CALL L38 LD A,259 POP HL CALL L171 EX DE,HL CALL L38 RET 13 Get a number from the code skipping over the floating point form which follows it. L81: LD HL,0 JR L83 L85: CALL L240 CP 126 JR 2,L84 PUSH AF LD DE,10	JP C,L252 SUB 28 LD B,8 LD C,A ADD HL,BC JR L95 L84: PUSH HL LD ML,(16486) LD C,5 ADD HL,OE LD (16486),HL POP HL RET INPUT statement. Find the variable and generate a call to read the Keyboard and then code to put the result in the slot reserved for that variable. L75: LD HL,L88 CALL 139	call to the Plotting routine. L60: LD A, ±98 L61: PUSH AF CALL L244 CALL L454 CALL L36 LD A, ±35 CALL L36 CALL L36 LD A, ±35 CALL L241 POP AF CALL L241 LD A, ±32 LD HL, ±4038 CALL L38 LD HL, ±4038 CALL L38 LD HL, L38 LD HL, 113 JP L39 14 UNPLOT statement. Much like PLOT but with a different argument	LD HL, 16436 CALL L38 LD A, 34 JP L38 IF tests start here. Arrive with DE=first argument, HL=second and A=operation. Result is carry set if condition true. L180: CP 221 JR Z,L182 CP 26 JR Z,L184 RRA JR NC,L181 EX DE, HL L181: RLA AND A SBC HL, DE
CALL L112 POP AF	CALL L240	L65: LD A, #A0	RET M
L83: CP 38 JP NC,L252 CP 28	JP L105 JP L105 PLOT statement. Find the pair of arguments and then generate a	JR L61 RAND statement. Gets number and puts it into location 16436. L31: LD A 42	RLA RET NC (listing continued on page 123)

(continued from previous page)

to write very compact machine code mainly because changes and corrections are nearly impossible so space is always left just in case.

However, it is impossible to fit an assembler, the target code and the source for more than 1.5K of machine code in a 16K machine. That is why the code was written in two longish sections — figure 1a. This left two options — write the code in more than two sections — two were hard to manipulate, three or more would be devilish — or bootstrap from Basic.

The concept of bootstrapping is to write a small compiler with features that allow you to write a larger one. At least one Algol compiler has been written that way. Just a kernel of machine code is necessary to get started and this already existed in ZXGT.

This route was avoided since the code generated by ZXGT is 16-bit integer and therefore would not be suited to the mainly eight-bit character handling of a compiler. Perhaps somone would like to write an eight-bit version of ZXGT — it would be even faster.

Now, you are probably asking why I did not buy a 32K or larger memory pack. The answer is that I had one but that ZXAS — the assembler I had — sat in the 27 to 32K slot so stopping Basic from crossing the 32K word boundary. For those who do not already know, it is quite permissible for Basic code — not just variables and arrays — to cross this boundary but at no time must the display file straddle the line, either side is equally alright. Also, you cannot run machine code which sits above 32K.

Salvation finally came when DCoder arrived on the market. DCoder is a disassembler but more important it is a relocator so it was possible to relocate ZXAS to sit above the compiler machine code — figure 1b.

Relocatable code is code which may be put and run anywhere in the machine. It is easy to write relocatable sub-routines but whole programs are harder. Suffice to say that neither ZXAS or ZXGT are relocatable.

So a relocator is a smart disassembler which can spot which jumps and calls are to within the code to be moved and change them appropriate to the new location. This does not make the relocated code relocatable but merely changes where it will run. The twist to this story is that DCoder was written independently by someone using MCoder — alias ZXGT.

The third major limitation was the Sinclair tape interface. I have rarely had any trouble with this but by the time MCoder II was completed 32K bytes of code had to be run back in each time the machine crashed. With the standard tape interface that takes about 15 minutes. This time the remedy was QSave which records 32K bytes in a little under one minute onto a standard tape recorder.

The final piece in the jigsaw, which arrived when MCoder II was nearly complete, was Ian Logan's ROM disassembly for the ZX-81. This allowed several new features to be added by using ROM routines to save space. ROM routines were only used where time was not critical eg. in Eptent. It also explained some of the clever but slow features of Sinclair's Basic.

In this section problem areas will be discussed in detail.

■ Strings: The major difficulty with strings was to avoid garbage collection when the string was lengthened or shortened. Eventually this was solved by decreeing that all strings would have a maximum length of 32 bytes. This length is held in a location that is known to the user so that it may be Poked from Basic.

The value is reset to 32 when the compiled program is run and must be Poked from your program. By this means the maximum length for individual strings can be set as the string is used for the first time. Thereafter the length of that string cannot be increased beyond that limit. There are no string arrays.

All strings — and numeric arrays for that matter — are allocated space at run-time and so sizes need not be preset. These strings/arrays are stored above Stkend. Consider this short program:

> 1 INPUT N 2 DIM A(N) 3 LET B\$ = "QWERTYUIOP" 4 LET C\$ = "BROWN FOX" 5 DIM X(N + N)

The program would prompt for N, allocate the array $A - 2^*N$ bytes to be zeroed – allocate B\$ and C\$ and finally allocate the array X of length 2N. Note the use of addition rather than multiplication to form 2N. Multiplication takes 10 times longer than addition and division 50 times longer. The result is the memory map shown in figure 2.

To concatenate strings, and that includes CHR\$, INKEY\$ or "string", it is necessary to form the new string elsewhere than its final resting place in case a string appears both on the left and right hand side of the assignment. For example:

LET A\$ = A\$(1 TO 3) + A\$(6) + CHR\$ 128 + B\$ + "*"

results in the new A\$ being built above Last -

	A array	B\$	C\$	X array	
STKEND STKEND + 100			LAST		Machine Stack

+2Nbytes+4 32 +4 32 + 2×2N

Figure 2: Dynamic allocation of space.

see figure 3 — and then locations Last to End are copied into A\$. The length of the string is simply the number of bytes between Last and End

Printing strings is a similar process except that the final copy is to the screen. A more difficult task is to compare two strings, for example:

IF A\$ <> B\$ THEN PRINT "NOT EQUAL" A copy of A\$ is made above Last — which is reset first — and a copy of B\$ is made above \pounds ad. Each is terminated with the code 255. With HL pointing to A\$ and BC to B\$, incrementing and comparing and using the results flags it is possible to do all six standard comparisons with one short routine.

■ Full variable names: Sinclair Basic is rare in allowing variable names of any length which may contain spaces if desired. Apart from disallowing spaces this is fully implemented in MCoder II. The variable names are stored as shown in figure 4. The names are entered downwards from just below the machine stack — actually 256 bytes clear of RAMtop — and the last letter is stored in inverse video, ie., bit seven is set.

For strings the names are considered to be A to Z\$ and for arrays A< to Z< thereby differentiating between the three sorts of variable. An example might be to store the names Fred, A\$, One and an array C, these would be stored down from just below the stack thus:



The zero on the bottom marks the end of the list. There can only be 255 variables as a single byte is used to store the number of variables. A careful note should be kept during compilation of the maximum number of concurrent variables together so that space may be reserved for them at the end of the compiled code.

In Basic the command Clear removes all the (continued on page 123)

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variables, but in MCoder the command merely zeroes all the variables used in the program. Zeroing the locations reserved for a string variable actually zeroes the pointer to the string implying that it has not yet been allocated.

Numeric arrays: As the DIM statement for an array is encountered by the compiler, code is generated to load the HL register pair with the length of the array - always one dimensional - and a call is generated to the "allocate space and zero" routine. This generates and stores a pointer to where the array is to be put and then zeroes the array as follows:

LD HL, where array is to be put $(=LAST)$	
LD, BC, length of array*2	
LD D, H	
LD E,L	
INC DE	
LD (HL),0	

LD (LAST), DE

If the array already exists then the old one is forgotten and a new one created. This failure to collect old, unused space can be wasteful but is not considered too detrimental.

Array elements are accessed by calculating the index, doubling it - two bytes per element - and adding this number to the base address of the array. No array bound checking is done as the code is assumed to be fully tested under Basic

Printing to the screen: To attain the same result as Basic is really only a matter of attention to detail. However, the Tab command may be of some interest. Sinclair stores the column number where printing is to take place next but stores it backwards. The actual column number stored is 33 - the number the user inputs. For example Tab 5 makes the system variable S_POSN (16441) take the value 28 and Tab 25 yields the value 8.

Printing to the ZX printer: If printing to the screen is clear, but perverse, consider Lprint. The printer buffer is in locations 16444 to 16476. Tab 5 makes PR_CC take the value 69 and Tab 25 makes it 89, these numbers being the lower byte of the address in the buffer. Otherwise the change from Print to Lprint commands requires only that bit one of location 16385 be set and then Sinclair's routines for Print, Tab, At etc may be used. Boolean operations: Only two Boolean

operations have been made available in MCoder II:

B AND C which is true if B and C are non zero B OR C which is true if either B or C or both are non zero

These are only implemented in the If statement using routines for Or and And which are called with HL containing one 16 bit argument and DE the other. They return a set carry flag if the result is true.

AND:	LD A,H OR L RET Z	;test if ;HL is zero ;return if it is
	LD A,D OR E RET Z SCF RET	;now do DE ;set carry flag ;return
OR:	LD A,H OR L JR Z, +2 SCF RET	;test if HL zero ;jump on if it is ;HL non zero, set carry
	LD A,D OR E RET Z SCF RET	;both HL and DE zero

Input: This had to be completely rewritten to be like Basic. For simplicity of coding input is limited to 31 characters plus the prompt so that it all fits on one line. This avoids the problem of compressing the display file. Invalid characters will not print, incorrect ones may be rubbed out, but you cannot step over characters using the cursors.

The screen is used as the input buffer and a string input is copied directly to the space reserved for the variable. The length can be ascertained from the position of the cursor. At the end the buffer is cleared using part of the array zeroing routine described above.

Speed: In ZXGT the particular If test to be carried out was decided at run-time. This resulted in very short code for the six functions. In MCoder II code is generated at compile time. As before HL and DE contain the two operands in the general form. IF operand1 operator operand2 THEN

and the code generated is:

LD HL, operand1 ; this may be the result of an

	evaluation
PUSH HL	
LD HL, operand2	
POP DE	
AND A	;clear carry flag
SBC, HL, DE	;subtract the two operands
	from each other
JP 'flag', NEXT	
	;code to be executed if the

condition :is true

;next statement after IF

NEXT: where 'flag' depends on the operator. For example <> requires the use of the zero flag. This results in considerably quicker code than in ZXGT, often by almost 50 per cent.

Brackets: Originally an arithmetic expression was terminated either by the end of line character -118 - or by a right bracket. The change required to evaluate an expression where brackets are not necessary - ie, like Basic – turned out to be very easy but needed the logic to be stood on its head. The expression is now not terminated by the four operators +,-,/,* and everything else means that the end has been reached, be it with a comma, semicolon, bracket etc.

Sinclair makes a distinction between the way that various functions decide where their arguments stop. For instance Peek 16388+1 means (Peek 16388)+1 not Peek (16388+1) but Poke 16388+1,n means the same as Poke 16389.n.

It seemed as if all the functions just picked up the next item, enclosed in brackets if necessary, while statements such as Poke, Plot etc., took expressions treating the comma or end of line as terminator. However, all was not so simple as Tab, which seems to be a function, takes the whole expression up to the next delimiter eg., Tab 5+3 means Tab 8.

Size: The final size of MCoder II at 3980 bytes is considerably larger than ZXGT/MCoder showing how expensive in space the refinements proved but it is a much easier program to use and its versatility is greatly enhanced.

I hope that this short series has given some insights into the way that a compiler works and what it is necessary to consider in the design.

ZXGT is marketed by Personal Software Services, 452 Stoney Stanton Road, Coventry CV6 5DG.



(listing continued from page 120)	EX DE, HL	INC HL	LD HL,≇929
moung continued nem page (ac)	LU A,210	LD A, (16396)	JP L36
JR L188	CALL LOG	SBC A,L	L234: CALL L233
L182: SBC HL,DE	LALL L240	RET NZ	LD A, ±D5
L183: SCF		LD A, (16397)	CALL L241
RET NZ	JP N2, L202	SBC A,H	LD HL,L171
JR L185	REI	RET	CALL L39
L184: SBC HL,DE	FAST and SLOW statements.	DIM statement. Make sure	LD HL, #D1EB
L188: SCF	Because the direct calls to the	argument is only single	JP L36
RET Z	ROM moved in the new ROM some of	dimensional. The size is ignored	Finally a jump list which links
L185: CCF	the ROM code is duplicated here.	as there is only one array. Note	to the jump list at the start of
RET	L40: LD HL,L103	that the array is not zeroed.	the first section. Although
IF statement. Generate code to	JP L39	L236: CALL L240	relatively expensive in space
get the two operands to be	L103: CALL ±02E7	CP 63	this method is pretty fool proof
compared and put operation in A	LD HL, ≨403B	JP NZ,L252	if any code is moved.
register. Generate call to " If	RES 6,(HL)	CALL L108	L151: JP #4088; PRNT
tests". On return test carry and	RET	XOR A	L131: JP #408B;SCROLL
jump to next line of code if its	L30: LD HL,L104	RET	L51: JP #408E;GETK
not set.	JP L39	16	L53: JP #4091;TESTK
L186: CALL L244	L104: LD HL,≇403B	These last two routines are used	L111: JP ±4094;PAUSE
LD H,A	SET 6,(HL)	in calculating the location of	L112: JP #4097;MULT
LD L.62	JP ±207	an array element. Basically the	L113: JP #409A;PLOT
CALL L36	Get address of next line. Enter	subscript is doubled and added	L114: JP ±409D;RST10
LD HL, L180	with HL pointing to the length	to the contents of STKEND	L115: JP ±40A0;±B6B
CALL L39	of the present line. Return with	(16412).	L116: EX DE,HL
LD HL,(16394)	HL pointing to the new line.	L233: CALL L108	JP ±40A3;DIV
INC HL	L50: LD C,(HL)	LD HL, #4BED	L117: JP #40A6;RND
CALL L91	INC HL	CALL L36	L80: JP ±40A9;GETNUM
EX DE,HL	LD B,(HL)	LD HL,16412	L187: JP #40AC;REM
CALL L171	ADD HL,BC	CALL L36	L241: JP ±40AF;SAVE