

Notes on the back story of this letter:

This 26-page (!) handwritten letter is *Melbourne PPC Chapter's John McGechie*'s reply to my previous letter to him dated 1980-07-15. John's handwriting takes some effort to decipher at times, but it's totally worth it.

He begins with some very detailed comments (several pages in fact !) about my attempt to create a **CP** (*Clear all Programs*) synthetics-based routine for the **PPC ROM**, including a number of suggestions on how to successfully implement it using his **b2** routine, which he'd submitted to **Richard Nelson** but hadn't been published (doh!), so he published it in the *Melbourne Chapter Newsletter*. He then discusses in great detail the various types of **END** and **.END.**, and his attempts to create a **RAM** test to check their locally-created double-density **RAM** modules.

As I'd asked how to access microcode, he next discusses this and mentions **Tom Cadwallader**'s method of identifying the addresses of the **ROM** routines by analyzing status register **Q**, as well as **Bill Wickes**' method to execute normal code in **ROM** starting at arbitrary addresses (and ending at the first **END** or **RTN** encountered), which John appraises as "*very ingenious*", explaining in detail why (and indeed it is !). On how to "*modify microcode*" (see my previous letter), he offers some comments but the less said about my naive question, the better.

He then addresses the "*NN catalogues*" (*Flag 30 Catalogs*) and his own exploring using his **KA** (*Key Assignment*) program, commenting on the weird behavior of assigned *numbers* (inserting the reverse of register **Q**'s contents as a text line in program memory, anyone ?) and **eGOBEEP**.

Next are his extensive comments about some aspects of the **PPC Club** and **Mr. Nelson**'s handling of it and his judgment on what to publish and what not, as well as some comments on the **PPC ROM**'s contents, inserting a "*shameless plug*" (XD!) for his own >1,000-byte word processing program for the **HP-41C**, and ending the main part of his letter at just 12 pages.

The letter doesn't end here, however, but continues (on *6th August* !) with two pages including additional detailed suggestions on how to make my **CP** program work, and more. He also included magnetic cards for his programs **KA(#14)**, **b2**, **ARA**, **AN**, ... everything but the girl !. Even the *P.S.* is long and meaty !

Surely the letter ends now ? Surely not ! These are just the first 14 pages, followed now by another 12 extra pages detailing his many experiments trying out the techniques and suggestions he discussed in the main part, and featuring extensively annotated thermal-paper printouts of everything but I won't discuss them here, this is just a *Notes*, examine them yourselves and enjoy.

John McGechie was an outstanding person by all accounts, including technically, philosophically and humanly. He was always enthusiastic, never had a bad word about anything, and would help anyone as best he could, even a 20+ newbie from 20,000 Km. away like me.

May he rest in peace.

Valentin Albillo, 21-12-2021



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Recieve

AIR MAIL

To V. Albilló

Padre Rubio 61-2^oC

~~6126~~ - Madrid 29

SPAIN



Cards in a few days - separately.



5th August '80

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DEPARTMENT OF PHILOSOPHY

Dear Valentin,

More detail on your long letter to follow up the short note sent yesterday. Since some of the matters you raise are quite serious for PPC I'll give them pretty careful attention - others are serious another way - for 4.1c! (Do you have the latest KA programs #13+14?)

(1) The Custom RDM and your routine. 'CP'

One way to clear all pgms without packing is to delete the ENDS, GTD .000 and XEQ, D, E, L, EEX, 999. This clears memory without moving .END. Useful when using B2 (as my "little b2" - do you have that?) using RCL b - into X and then XEQ b2. The contents of

Y are stored in the register where RCL b was effected. Much better than Bill Wickes version - about 1 second, and dont need to know address at all. (about 70 bytes - good one for RDM I think !!) (Published in our local Newsletter last March - but Richard hasn't published !)

According to Charles Close there are 3 kinds of END's and correspondingly 3 kinds of .END. Byte 3 is:
Packed by PACK Packed by GTD.. not packed - dictated by PGM entry

END	09	20	0D
.END.	29	20	not sure

He is not quite clear in writing on this and I've not yet tried his suggestions - but programming it is a good scheme (Why do so few PPC members try this?)

Apparently I was the only one to try programming key assignments. Why? (I have over 20 different programs from the original bug 2 version of January 2nd this year) (written up in those 16 pages - I'll send a copy if you wish)

I've not yet analysed your program (I will next week when Univ. classes end for 3 weeks & will have some spare time) but you say you put the new .END. at the "top of program memory" Where? It should not be in R00 but below it.

See point 10

That "LBL 10, LBL 14" sounds familiar. Several times when I've disturbed an END or alpha label (global) I've lost program and found a 'LBL 14' (or is it?) which will not erase. That's probably a remnant of the inserted .END.

Where is "CP" put in memory? At the top?

Have you tried altering it so that the .END. is put in just below the END of "CP"?

Remember that there should be ~~a~~ a hex digit set in the .END. to link it to the certain address. Preserve the

'Chain' intact and it may work - ie make your entered .END. include the link to the certain address. (I'll try to work out what the format should be and see how to modify your program to make the

(3)

right .END. C0/00/ZD should contain, perhaps

Bits \rightarrow ~4 ~3 ~9

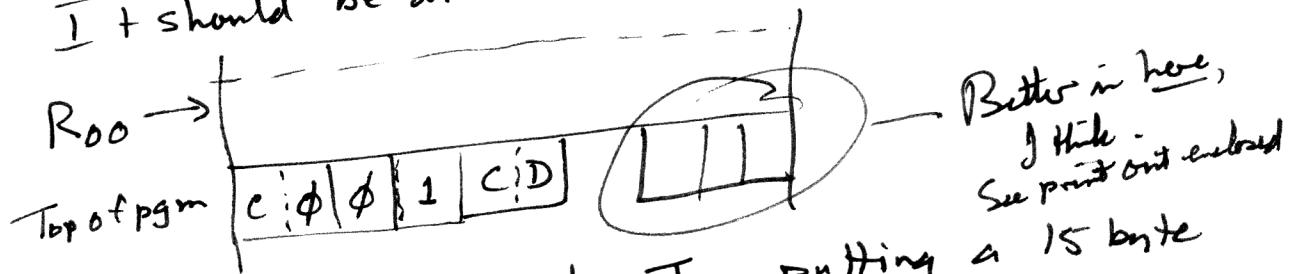
C				CID
---	--	--	--	-----

now bytes to contain

So if contain is at (say) OFF (size 000
on Basic 41c) and the .END. is dumped
exactly one reg below, it should then be

C	1000 0	0000 0	0001	CID
---	--------	--------	------	-----

I + should be at the LHS of register = perhaps



I've not checked this! Try putting a 15 byte
jump instruction (eg CLD) at line 00, then
.END. next to it, byte jump, and analyse
alpha (you know that the bytes jumped are
read out into alpha?) I'll enclose my new
DECODE for you to use. Do the byte
jump, read in my "AN" then RCL M,
XEQ "AN". Will let you know.

See printout.

Then knowing the structure needed, you can
redesign your "CP" to make the right NNN.
It may be that your $x^{last} < > c$ should be a
STD C? The STD IND Z (in the artificial
Reg C contents) may derange the following
 $x < > y$, $x < > c$, RTN?
Lots to look here!

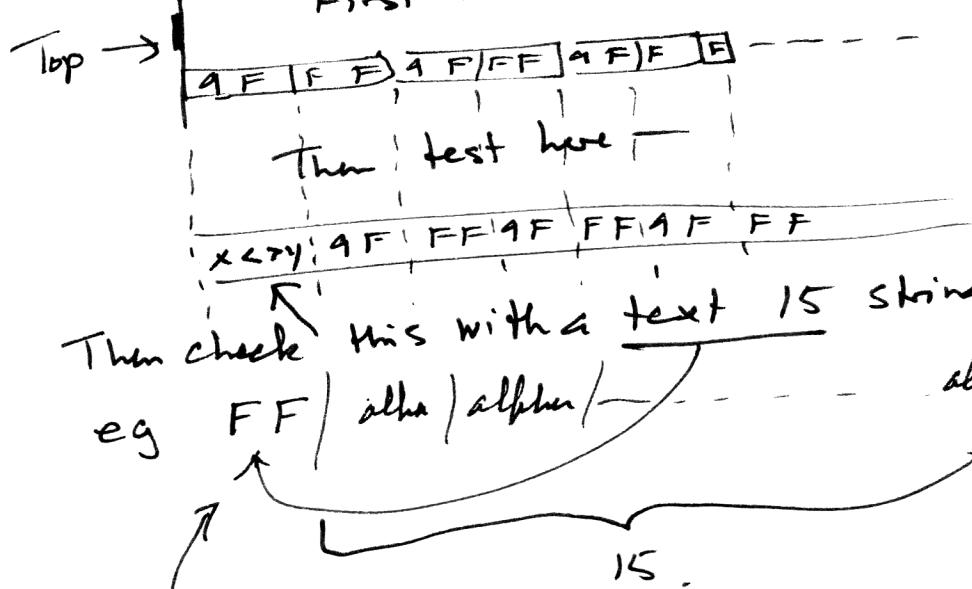
RAM tests.

As you'll have read by now, I had to devise RAM tests to check the locally made D) modules (I have one - will have two very soon)

First one was exhaustive, but can't program it quite.

Tone IND e is 9F/FF. Size to
000 (top of DD module on trial) and Fill it
with Tone IND e. Print out. Then
put an X<=>Y at top of memory, PACK,
print out.

First test here —



Then check this with a text 15 string at the top —
all the

Then check for s
eg FF | alpha | alpha | - - - - - alpha

That checks the first byte.

So all bits checked in program mode that way.

Next - an NNN (I forgot which I chose - had this)

So all bits checked ...

Next - an NWN (I forgot which I chose - had this as an alpha string: /F7/FF/FF)FF/FF/FF)FF/F₂
read ~~it~~ into Reg M.

RCL M produces the NNN

$FF|FF|FF|FF|FF|FF|FF$
 in $X \in S_{TD} \rightarrow RCL$ this is every reg and

on a SIZE including as the lowest numbered data reg. one known to be good (I used a SIZE of 001 on basic 41c, did a STO, RCL in R00 to get normalised NNN - an alpha string

which is, I think, 10/FF)FF/FF/FF/FF/FF
This is $x=y?$ on every register's normalised version of the FF)FF/FF)FF)FF)FF/FF
If one does not produce the same, alarm bell rings, whistles blow, etc.

Now another ^{two} to store & recall is negative

and MSD of mantissa normal numbers to check sign digit. That leaves two bits in the sign digit untested. in this STO/RCL mask - but they are not used for STO/RCL, anyway, of data.

I have these on cards and Graeme Davies has combined the ~~3~~ STO/RCL cards now times into a single program. Takes about 1 minute to run I think. (? or 8 minutes?)

I can't think of any more tests - the mantissa in one STO RCL is 7 (bit pattern 0111) and the other is 9 (bit pattern 1001) to test all bits. Only bits 2 and 3 of sign digit not tested - but NNN normalise probably

checks them.

Any ideas?

Used a counter in the stack.

Prints as
"rrrrrrr"
from Print reg.

Accessing microcode

I doubt whether this can be done. Tom Cadwallader had identified the addresses of the ROM routines via analysis of Register Q (I'll enclose his details) and it may be possible to do something with this.

No - send
later if you
wish - give
little too
long already!

Bill Wickes ROM access accesses normal code in ROMs (I have a routine ARA which was designed to do this, but didn't hit on Bill's way round the problem - ARA - jump to any RAM address and can be initialised to go directly to any address in RAM, XEQ from location addressed to a RTN or END . In use, RCL b, STD M,

\uparrow
at start location

ASTD nn - in a reg. For (say) 10 addresses, do a RCL b, STD M, ASTD 00, --- ASTD 01 --- ASTD 09. (Oneboxed card "ARI" with "ARA")

Then 1, XEQ ARA will XEQ from 1st RCL b location to RTN and come to line

after 1, XEQ ARA, etc. 2, XEQ ARA does the second etc. (The Reg b address is

received by CLA, ARCL IND X,

RCL M) Wouldnt jump to a RCL b'd

ROM address ! I sent details to Bill,

and 4 weeks later back came his ROM access ! I've not worked on ARA since !

This method very ingenious - the jump into ROM is effected via an artificial return stack with the ROM address one up in the stack. I had tried just a STO b of the ROM address.

Read Bill's article very carefully. The form of ROM addresses in Reg b is different. I'll add details of some decoding I did on the printer Rom routines (normal code) and the WAND routine. I don't quite understand their addressing system yet. There's so much to be done!

Modifying ROMs? Sounds a bit dangerous!

The ROMs in the 41c are not magnetic but in microchips in Sapphire on Silicon. Hardwired! If you could push bits into them and change - you may destroy them! They are not the EPROMS of computer type.

But an analysis of the access code of the 41c via the ports may be feasible?

Remember the work on the 67 in late 1978? See also the HP article on microcode in the HP Digest no 3. (Can you get hold of it? Can send a PC if not.)

The HP 85 may use EPROMs (it's a small computer, though compact)

RTN format
the same as
RCL b format
unlike RAM
RTN addresses
in RTN stack.
See Bill W's
Guru column
article in
PPCJ.

No!
This later
also.

I think the NV catalogues of the 41c are the normal catalogues with the readout alpha deanged. This pattern follows the normal catalogues. I copied out many of those catalogues last year and found some that went through the same cycle. Where one starts in ^{some of} the catalogues depends on the settings of flags - I forget which now.
 (in the 30's from memory) The resemblance to Synthetic prompts is accidental, I suspect.

I have an enormous sheet of paper with 256×256 boxes on it sampling the possible 2 byte assigned functions.

Try IND on some with prompt lines.

Even two byte assignment can be made by KA - but there are 256×256 of them !! I've sampled the lower numbered ones. Assigned numbers (not alpha' numbers) are useful - they put unterminated numbers plus the reverse of Reg Q in an up to 7 byte hex + line into program. I will be writing this up very soon - if you wish I'll send a quick summary. (Attached in printout!)

See printout!

I doubt (here I go again!) whether there is a STD -- which will produce the Status functions - you would need to fill the prompts with hex digits - and then don't enter from the number keys! If they did, high numbered XRM's could be made. The highest we give by &GOBEEP -- with IND 99. The IND (shift key) makes them jump up by 128. (Details if you wish - but put &GOBEEP on a key - exhibit! Other XRM prefixes produce fewer XRM nos. - or often only one.

PPE comments

I think you are a bit unfair about . Sure -

Some in the Journal is of little value to many. But some is not. Many think the NNN stuff should not be there -- about 80% is of no use to me, another 80% is of no use to another number, and so on.

What is valuable? Remember Dave Kemper's discovery (one you made independently) of the std routine return stack on the 67? See what that led to!!

even in
a disguised
form!

Can use KA
to make any
individual one
on a key, though.

Some take
alphanum

Richard Nelson doesn't understand most that he receives - some quite awful stuff I know. I sent material on formal logic programming two years ago - an utterly new area (about 8 people I now know of have independently done very interesting work in the area) and offered it to Richard. No response. It's since been published here.

John Kennedy read a copy I sent to Richard, and wrote very excitedly to me about it - he thought he was the only one too - and we have corresponded ever since.

Much is just for fun! (Programmable calculi are fun!) Some is mathematically interesting, some are useless (who wants 100 decimal points of π , or alphabetics or-----) What will be fruitful? We can't judge always.

And Richard does work his tail off for PTC. He is at it 7 days a week - comes home from work - eats his tea - has a short sleep, then at the Clarendon from 9pm to 3am every night. He does it all!!

That so much good stuff is published is a miracle I think. Having had grizzles here in Melbourne over the contents of our Newsletter (all of which I've written for most months) from those who have done nothing toward it, I've some idea what he must do. He should have editorial help, but doesn't very often.

All one can do, I guess, is send in material neatly finished in copy ready form, brief as possible, clear as possible, etc. I think his judgement could often be better - but my own has its lapses - - -

As to the RDM contents - - - what do PPC membership want of the RDM? Programs in it useful to many. But the range of conflicting interests! Look at the Users Library programs. Which of those 7000 would you want in a RDM? Why? Send them

Sub routines
to assemble into
your programs
but - but
not with those
damned long
alpha labels that
HP use!

'wish list' of what
you think desirable. I think alpha sort
routines, as flexible as possible, are
probably the most valuable - and, for
example, extended RAM (for d' Areas
stuff) access routines for bug free
41c's is very desirable.

And what of routines that would allow the printer to Graphics do pictorial work? A multiple function plot of sequential computation of functions — why not? Animated displays via plots on the printer? And the recording on micro cassettes of your program library accessed under program control? (That's where the latest word work points)
I don't think the PPC ROM will suit every user. I suspect that $\frac{3}{4}$ of its programs won't be wanted by me — but then again — must close for now. Many more things in your valuable packet of material I'll cover later — but may I use some of this in our Technical Notes? I'll assume you've no ~~objection~~ objection to my reprinting it there?

Yours
John
(3324)

I have a word processor program for the 41c — about 1000 bytes at least is needed. That would be a wish I would have for a ROM! Will be written up soon.

6th August!

13

Here is the check up. I think your routine will work if

(1) You run it

(2) Get above inserted .END.

(3) Insert an instruction to unblock ~~E~~ inserted

.END. - or at least to jog/jar/distract it (!!!)
and then pack. You may need to have its chain
to top of memory correct, though it looks as though
this is not necessary. CΦ/ΦΦ/2D, an 'unblocked'
.END. may be the one to store.

CΦ/Φ1/29 didn't work apparently -
led to lost memory - but haven't tried an inserted instruction
above it followed by a PACK.

CΦ/Φ1/2Φ with 7 places above it went PRIVATE
and I think this is an END, not an .END.
(Charles Close vague about this) but PGMS cleared -

Had to raster clear afterwards.

String ΦΦ/ΦΦ/ΦΦ/ΦΦ/CΦ/Φ1/2Φ

and inserting a byte (+) above it, cleared
pgms ok, ~~but~~ twice! Turned into a
works - ENDs

CΦ/Φ1/C D/ΦΦ/ΦΦ/ΦΦ/ΦΦ

at top of memory cleared pgms ~~but~~ when a +
inserted above it and then a PACK but left
junk in pgm - a RCL 13 that caused trouble.

SO !

I think your answer is this:

Make your NNN to put ~~be~~ immediately below
Roo in the form

$\phi\phi/\phi\phi/\phi\phi/\phi\phi/c\phi/\phi_1/2\phi$
Bytes 1 2 3 4 5 6 7

where bytes 5 to 7 are those of an ~~unpacked~~ END
(packed by GTO - according to Charles Close) and putting an
instruction above it, the machine clears high, puts in
only that instruction before the .END. that
results.

I have still not analysed your program -
spent some time trying & showing you all this.
I think "AN" is a good routine - with byte
jumper to read out pgm bytes lots very quick -
only about 1 second.

I hope this all helps ! Took a lot of time
to do and to annotate - so just see you
appreciate it !!

I'll send cards of KA #14, b2, ARA, AN
etc for you to try. (Very proud of AN - ~~you~~ you should
take credit for making me think of it !)

John

Remember the END and .END. formats ! See 1st 2
Corvallis Columns in PPCJ. Have to check up on
Charles Close's data too. But later on !!!
You nice to have another "foreign correspondent" for our local Chapter !

(1)

First I read three pgms
into the 616

```
CAT 1
LBL KA
LBL KC
LBL NN
END    287 BYTES
LBL b2 Synthetic B2
END    93 BYTES
LBL AN Natural (file) language NNN decode
END    75 BYTES
LBL ARA Auto RA Marcos
LBL IRA Initialisation for ARA
END    53 BYTES
LBL ROM Bill Wickes ROM Marcos
END    57 BYTES
.END.   09 BYTES
```

This is the #14 (printer) version

First making some key assignments
needed at the start.

	XEQ "KA"
PRS?	5. RUN
KEY1.	241. ENTER↑ 62. ENTER↑ on ε+
<i>Byte Jumper (one such possible)</i>	11. RUN
KEY2.	144. ENTER↑ 124. ENTER↑ 12. RUN
Recall b on 1/x	145. ENTER↑ 117. ENTER↑ 13. RUN
KEY3.	
STO M	144. ENTER↑ 117. ENTER↑ 14. RUN
on	
KEY4.	
RCL M	144. ENTER↑ 117. ENTER↑ 14. RUN
KEY5.	
S ass. to LN	4. ENTER↑ 21. ENTER↑ 15. RUN
KEY6.	145. ENTER↑ 121. ENTER↑ -13. RUN
STO Q	
KEY7.	PRKEYS

USER KEYS:

```
11 XROM 04,62
12 XROM 01,60
13 XROM 05,53
-13 XROM 05,57
14 XROM 01,53
15 X
25 "AN"
01+LBL "TST"
02 E0
03 RCL b
04 STO E
05 RCL L
06 X
```

Byte Jumper
RCL b
STO M
RCL Q
RCL M
S ass to key
NNN analoger.

alpha print
after RUN
of b2

PRP "TST"

01+LBL "TST"

02 "+"

03 RCL b

04 STO E

05 RCL L

06 5 }

07 "

08 STO 14

09 .END.

Text line - one null

*put in by the S ass to key -
see later.
and this!*

09 STOP — *loops! not really intended*
02 STO _
STO Q

02 +

03 +

04 +

05 +

06 +

07 +

08 +

09 +

10 +

11 +

12 +

13 +

14 +

15 +

16 +

Some filled

bytes as

a storage

location

for later.

PRP "TST"

01+LBL "TST"

02 +

03 +

04 +

05 +

06 +

07 +

08 +

09 +

10 +

11 +

12 +

13 +

14 +

15 +

16 +

17 .END.

OK? So

this is how
it now is!

↙ this instruction.

Address of —

XEQ "NN"

— 33. RUN

— 0 111. RUN

— 108. RUN

— 108. RUN

— 105. RUN

— 98. RUN

— 65. RUN

GTO "TST"

— GTO .010

RCL b

XEQ "b2"

Y=NNN, X=Rb? ↙

RUN

"P:i0Rb" ***

*i0Rb ↙

GTO "TST"

PRP "TST"

Prompt line to
warn that correct
format is in stack.

Print after RUN

2

01 *LBL "TST"
02 +
03 +
04 RCL 01
05 OCT
06 HMS
07 HMS
08 FRC
09 FACT
10 -
11 +
12 +
13 +
14 +
15 +
16 +
17 .END.

A b : 1 1 0 1

Actual Reg b was recalled this is the date where

Artificial B2 storage - OK?

02 X this is the program entry form of 5
PRP "TST" assigned to a key.

Note indentation →
Some of the bytes had
case in lower
of table.

01+LBL "TST"
02 5
03 ** } *he is the result!*
04 + *Where from?*
05 + *Aha!*
06 RCL 01 }
07 OCT } *Still there!*
08 HMS }
09 HMS }
10 FRC }
11 FACT }
12 -
13 +
14 +
15 +
16 +
17 +
18 +
19 .END.

-1.757272696-59
02 X

"A"	RDN	Stack from "b2"
**	RDN	
**	***	
	RDN	

	STO -	that's the b2 stored NNN still there
GTO "TST"		store NNN in RegQ(wait!)
PRP "TST"		

Put 5 in
pgm from ass.

06 +
07 +
08 RCL 01
09 OCT
10 HMS
11 HMS
12 FRC
13 FACT
14 -
15 +

16 +
17 +
18 +
19 +
20 +
21 .END.
 CTD 201

PRP "TST"
61+LBL "TST"
62 E

Store NNN in Q
again.

← Wow!!

01 LBL "TST"
 02 5
 03 "Abillo!"
 04 5
 05 "TST"
 06 5
 07 "x"
 08 +
 09 +
 10 RCL 01
 11 OCT
 12 HMS
 13 HMS
 14 FRC
 15 FACT
 16 -
 17 +
 18 +
 19 +
 20 +
 21 +
 22 +
 23 .END.
 GTO .001
 STO -
 02 X
 03 X
 04 X
 05 X

Print 4

→ { 02 x
03 x
04 x
05 x } Put in 4. 5's brass. key.

PRP

01 *LBL "TST"
02 5555
03 **
04 **
05 **
06 "Abillo"
07 5
08 "Abillo"
09 5
10 "TST"
11 5
12 **
13 +
14 +

Reg# is
emptied
by first
5 (ie *)

Get the idea?
Assign E to
a key and the
E's chain up -
So do decimal
points.
E and key
followed by
a number key
puts E 3
(EEX, ³)
in pgm.
See KA
below.

GTO "KA"
 GTO .000
 01 XEQ 05
 01 LASTX
 LIST 010
 01 LASTX = hex 76
 02LBL "KA"
 03 "PRS?"
 04 PROMPT
 05 E ← fast than 1
 06 -
 07 E3 ← puts 1000 in X
 08 /
 09 STO 02
 10LBL "KC"
 GTO .002
 PACK
 Σ0
 of 6 bytes
 Byte jump while
 'on' line 2
 above
 RCL I
 XEQ "AN"
 0.120015300 ***
 00<00023004;41 ← print of no.int
 0.000000000 *** 14-digits -
 RDN *** see left.
 RDN *** Y
 RDN *** Z Stack contents after "AN"
 RDN *** T
 GTO "KA"
 GTO .001
 DEL 001
 GTO .000
 ← remove LASTX
 We will now analyse
 this END
 Get back at
 top of pgm
 memory
 01 LASTX
 02 END
 GTO .002
 Σ0
 Alpha printed
 6 bytes jumped.
 Hex analysis of X
 0.0<0000=0000
 0.0<0000=0000
 notice here
 digits not
 normalised
 in print (and
 display)
 mistake done
 in RVN mode
 ALPHA DATA
 01 +
 02 +
 03 +
 04 +
 05 +
 06 +
 07 +
 ← cut top of pgm memory
 PACK
 NB a pack here
 01 +
 02 +
 03 +
 04 +
 05 +
 06 +
 07 +
 08 LASTX Hex 76
 09 END
 GTO .009
 Σ0
 Byte jump.
 alpha reg 6 bytes
 Recall M
 X reg
 1.200010260+?3 ***
 XEQ "AN" ***
 00<00009<600?3 ← Print of X
 National language
 (41c) head digits
 CAT 1
 GTO .000
 PRP **
 get back in top
 pgm file
 to "un pack"
 01 XEQ Y
 02 +
 03 +
 04 +
 05 +
 06 +
 07 +
 08 +
 09 LASTX
 10 END
 GTO .010
 Σ0
 alpha
 RCL I
 1.200014260+?3 ***
 XEQ "AN" ***
 00<00009<600?3
 CAT 1
 GTO .001
 CLP **
 X reg NNN
 X reg NNN analysis
 END is now
 CΦ/ΦΦ/ΦD
 Clear top file and END.
 Put 7 + at top of memory.
 PACK ← NB!
 GTO .001
 LIST 010
 01 +
 02 +
 03 +
 04 +
 05 +
 06 +
 07 +
 08LBL "KA"
 09 "PRS?"
 10 PROMPT

(4)

XEQ "NN"
 GTO "KA"
 GTO .000
 RCL b
 XEQ "AN"
 0.00000000-44 ***
 000000000016<
 GTO "KA"
 GTO .001
 RCL b
 XEQ "AN"
 0.00000000-44 ***
 000000000016< Same address! (SIZE = 20)
 2 modules in
 GTO "KA"
 GTO .002
 RCL b
 XEQ "AN"
 0.00000000-45 ***
 000000000016< < byte 6 of reg. 16 A =
 address of +
 in line!
 GTO "KA"
 GTO .003
 RCL b
 XEQ "AN"
 0.00000005-45 ***
 0000000000516: address of line 2, bytes
 XEQ "KC" "KA continue"
 KEY7.
 Recall 144. ENTER↑
 Shifted LN C
 KEY8.
 Recall 125. ENTER↑
 Shifted LN
 KEY9.
 -14. RUN
 144. ENTER↑
 122. ENTER↑
 -15. RUN
 RCL c
 "pziAQ" ***
 XEQ "AN"
 "pziAQ" ***
 1770016916<111
 ← analysis
 ← region hex 177
 (Reg 10)
 "cold start" const.
 address of END.
 GTO "KA"
 GTO .000
 RCL b
 XEQ "AN"
 0.00000000-44 ***
 000000000016<
 PACK
 GTO "KA"
 GTO .000
 RCL b
 XEQ "AN"
 0.00000000-44 ***
 000000000016<
 STO I
 RCL c
 "pziAQ" ***
 XEQ "AN"
 "pziAQ" ***
 1770016916<111
 ZREG 00
 RCL c
 XEQ "AN"
 "pziAQ" ***
 16<0016916<111
 ← register = Reg c
 ← register = Reg c
 ← sign digit of 1 makes
 recall an alpha string.
 ← mistake!
 top of file
 address of Reg 16C
 Byte' φ,
 register Hex
 16C
 = 16² + 6x16 + C
 = 256 + 96 + 12
 = 364 decimal.
 PACKING
 RCL c
 XEQ "AN"
 "pziAQ" ***
 16<0016916<111
 GTO "TST"
 GTO .029
 END
 28 LASTX To jump 6 bytes
 GTO .029
 PACK
 L
 GTO "TST"
 GTO .029
 END
 29 CLD To jump 15 bytes
 PACK
 GTO .030
 END
 LA)*****
 11 bytes -
 so first 4 were
 nulls
 ASTO 10
 CLA
 ARCL 10 } remove some
 of RH nulls from
 alpha-ptr
 LA)*** }
 RCL C
 XEQ "AN"
 0.00000000-88 ***
 00<<0829000000
 GTO "TST"
 RCL b
 XEQ "AN"
 0.00000000-80 ***
 0000000000211:
 address of first END before LBL TST
 GTO "TST"
 GTO .031
 RCL b
 XEQ "AN"
 0.00000000-88 ***
 0000000000112
 Byte before final END.
 Byte φ of 112
 9regs below
 END before
 LBL TST
 ← error - not in TRACE mode
 CAT 1
 CAT 1
 LBL KA
 LBL KC
 LBL NN
 END 294 BYTES
 LBL b2
 END 93 BYTES
 LBL AN
 END 75 BYTES
 LBL TARA
 LBL TIRA
 END 53 BYTES
 LBL ROM
 END 57 BYTES
 LBL TTST
 END 65 BYTES
 PRP "KA"
 Next page for a
 listing for you.

08*LBL "KA"
"PRS?" PROMPT E -
E3 / STO 02

16*LBL "KC"
17 FIX 0
18*LBL 06
PRP "KA"

01 +
02 +
03 +
04 +
05 +
06 +
PACK
PRP "KA"

This is KA #14 -
a printer version -
compact and
very fast - leaves
19 regs on basic
41C for assignments.

01*LBL "KA"
02 "PRS?"
03 PROMPT
04 E
05 -
06 E3
07 /
08 STO 02

09*LBL "KC"
10 FIX 0

11*LBL 06
12 ""
13 RSTO 00
14 .5
15 XEQ 07
16 E
17 XEQ 07
18 RCL 02
19 RCL c
20 RCL I
21 "xip" -
22 ASTO c
23 STO IND Z
24 RDH
25 STO c
26 ISG 02
27 GTO 06
28 BEEP
29 "WSTS"
30 PROMPT
31 WSTS
32 RTH

33*LBL 07
34 RCL 02
35 INT
36 +
37 ENTER↑
38 +
39 "KEY"
40 RCL X
41 TONE 9
42 PROMPT
43 STO 01

Woops!
in Trace mode!

I cleared these
by backarrow
and
Packed.

44 RDH
45 STO 03
46 RDH
47 XEQ 00
48 -24
49 X<> 03
50 XEQ 00
51 10
52 ST/ 01
53 RCL 01
54 ABS
55 INT
56 LASTX
57 FRC
58 ST* Z
59 .1
60 -
61 X=0?
62 GTO 05
63 RDH
64 4
65 X=Y?
66 ISG Z
67*LBL 05
68 RDH
69 STO Z
70 X<>Y
71 8
72 X<>Y
73 ST* Y
74 RDH
75 +
76 CHS
77 44
78 +
79 ST+ 03
80 SF 25
81 SF IND X
82 DSE Z
83 ""
84 X<> 01
85 X=0?
86 GTO 01
87 RCL I
88 XEQ 03
89 STO I
90 GTO 08

Byte number for key code
in ass. reg now in X.

Correction
for row 4
here →

You know how
this part of
KA works
do you? Considering
flag no. and
byte no. for
key chosen from
its key code -
in stack at
same time!
Very tough
stack analysis
to work this
out - took
about 30
or so hours of
working out
last February.
} check for flag no over 29.
FΦ (Test Φ)

set flag ind for t or r
(unshifted keys)

91*LBL 01
92 8
93 ST+ Z
94 RDH
95 RCL e
96 XEQ 03
97 STO e

set flag ind for e

98*LBL 08
99 RDH
100 RDH
101 X<>Y
102 16
103 *
104 +

Shifted
keys
here

When this is ASTO'd
in c, c contains
1Φ|ΦΦ|ΦΦ|69|ΦΦ|ΦΦ|BF
I notice you use BF
also - cΦ is ok also -
also stores 1st ass.
still stores 1st ass.
in dΦ. Using ASTO c
in dΦ. Using ASTO c
saves lines and
bytes!!

F5|d|69|ΦΦ|ΦΦ|BF
Closes up - no print
prints as space in
text line
Flag no in
corrected in R03
and X →
Prints here as
space space
ie alpha
string of 4
bytes.

105+LBL 00
 106 128
 107 X>Y
 108 X?Y?
 109 GTO 10
 110 X>Y
 111 -
 112 ENTER↑
 113 ENTER↑
 114 127
 115 BLDSPEC
 116 X>Y

 117+LBL 10
 118 BLDSPEC
 119 CLA
 120 X>E
 121 ASHF
 122 ASTO X
 123 CLA
 124 ASTO Y
 125 ARCL 00
 126 ARCL X
 127 ASTO 00
 128 X?Y?
 129 RTN
 130 "F+"
 131 ASTO 00
 132 RTN

 133+LBL 03
 134 FS?C 25
 135 GTO 09
 136 CLA
 137 X>X
 138 "F***"
 139 X>X
 140 X> d
 141 SF IND 03
 142 X> d
 143 X>X
 144 "F****"
 145 X> I
 146 RTN

 147+LBL 09
 148 X> d
 149 SF IND 01
 150 X> d
 151 RTN

 152+LBL "NN"
 153 CLA
 154 ASTO 00

 155+LBL 02
 156 STOP
 157 XEQ 00
 158 RCL E
 159 GTO 02
 160 END

Synth.B3
needed?
If Flag 25
clear it is.

{
 134 FS?C 25
 135 GTO 09
 136 CLA
 137 X>X
 138 "F***"
 139 X>X
 140 X> d
 141 SF IND 03
 142 X> d
 143 X>X
 144 "F****"
 145 X> I
 146 RTN

}
 147+LBL 09
 148 X> d
 149 SF IND 01
 150 X> d
 151 RTN

}
 152+LBL "NN"
 153 CLA
 154 ASTO 00

 155+LBL 02
 156 STOP
 157 XEQ 00
 158 RCL E
 159 GTO 02
 160 END

Synthesised
 Bug 3 to
 Set flags over
 29 (L H column
 keys). Flag
 no was cleared
 in R03

}
 Universal NNN
 generator -
 not part of
 KA - but
 uses LBL 05
 (from 105 to
 132)

}
 Synthetic B3 not
 needed

Very fast, short,
 BLD using
BLD SPEC!

When $x=0$,
 empty alpha
 string in X
 see test at
 line 128

When y is a normal number (any)
 and $x = 1$ to 127 , BLDSPEC
 puts byte 1 to 127 in X and
 lifts stack - $x+y$ lost, $y=3$
 and T and $Z = t$.
 When 't' ie byte 127 in Y and
 ϕ is in X , BLDSPEC produces
 Byte 129 in X (as alpha char)
 't' in Y , 1 in $X \rightarrow$ Byte 129
 etc upto 't' in Y , 127 in X
 produces byte 255 in X as
 R.H. Byte. This BLD uses this printer
 ROM function! Very proud
 of this one - short and fast.

This ensures that only
 right most byte in X is
 isolated - When 't' in Y . (Produces 2 bytes in X for byte no over 127)
only right most wanted.

PRP "b2"

 01+LBL "b2"
 02 TONE 0
 03 TONE 9
 04 "Y=NNN, X=Rb?"
 05 PROMPT
 06 CLA
 07 X>E
 08 "F****"
 09 X>E
 10 X> d
 11 CF 00 ← Probably
 12 CF 01 not needed
 13 CF 02
 14 CF 03
 15 SF 07
 16 SF 08
 17 X> d
 18 FRC
 19 CLA
 20 X> E
 21 "F+"
 22 CLX
 23 STO X
 24 "F++"
 25 X> X
 26 X> E
 27 ASTO X
 28 "x1"
 29 ARCL X
 30 "F"
 31 RDN
 32 RCL C
 33 X>Y
 34 ASTO C
 35 STO 00
 36 RDN
 37 STO C
 38 RTN
 39 END

necessary!
 warning!

This is the ~~old~~
 automated B2 for
 non-B2 machines.
 Bill Wickes
 method automated.

← Enter pgm with
 # contents to be
 stored in pgm
 in Y address
 via RCL b in
 X and dumps
 Y at that
 address.
 3 right most digits
 from RCL b
 isolated - shifted
 one digit left
 (FRC), made into
 uv/wφ in X
 uvw

F2/01/69

Append BD (I think!)
 nowhere 01/69/uv/wφ/BD
 in d.

PRP "AH"

```

01+LBL "AH"
02 FIX 9
03 FRX
04 XEQ 01
05 XEQ 01
06 XEQ 01
07 *****
08 ARCL Y
09 ASTO Y
10 ***
11 ARCL Y
12 RSHF
13 ASTO Y
14 CLA
15 ARCL T
16 ARCL Z
17 ARCL Y
18 PRA
19 STOP

```

```

20+LBL 01
21 CLA
22 X> I
23 *****
24 X> I
25 *****
26 RCL \
27 ***
28 ARCL X
29 RSHF
30 ASTO X
31 X< Y
32 END
XEQ "ARA"

```

ALPHA DATA

PRP "ARA"

Woops!

Enter with inst.
address of reg. with
REL b
address in it.

Use, eg, 5, XEGARD
in pgm, and goes to
RAM address put in
Reg 05 by IRA.

```

01+LBL "ARA"
02 STO L
03 RDH
04 CLA
05 RCL b
06 X> I
07 *****
08 STO \
09 *****
10 X> \
11 STO I
12 ARCL IND L
13 RDH
14 ASTO b
15 RTN

```

```

16+LBL "IRA"
17 CLA
18 STO C
19 ASTO IND Y
20 AVIEN
21 RTN
22 END

```

Initialise - Rel b
at wanted entry location,
put reg no in X,
RCL b at wanted
location, XEQ IRA.
To remember that
location, key in Reg-number,
XEQ ARA.

This is the analysis routine, in 4/6 hex digits
of any no in X. Much faster & shorter than Bill W's
various 'Decode' versions - suggested by you
decode into octal digits - easier to read.

Must be FIX 9 - Hex digits always show
in display if exponent < 10 (including OA etc)
so break up 14 digits in alpha into blocks,
ARCL X and isolate blocks of 6 at a time -
as alpha in stack and reusable after last one
is cut to 1st two characters ($6 + 6 + (6-4) = 14$)

\leftarrow isolate 6 digits from X - the left most 6.

Bill Wicks ROM.
In Journal.

PRP "ROM"

```

01+LBL "ROM"
02 SF 01
03 STO E
04 CLX
05 RCL b
06 X> I
07 FC?C 01
08 RTN
09 "ABCDE"
10 X> I
11 STO a
12 X> \
13 X> I
14 ARCL 10
15 X> I
16 STO \
17 CLX
18 X> a
19 X> I
20 "FAB"
21 STO I
22 X> \
23 STO b
24 END

```

Does
what my
ARA
failed to do.

END and -END analyses.

8

```

GTO "TST"
GTO .002
RCL b
XEQ "AN"
0.000000002-80 ***  

0000000000211: Byte 2 of Reg. (hex) 11A
GTO "TST"
30 + Extra byte occupied in TST
      PACK
PACKING
PRP "TST"

```

01*LBL "TST"

02 5555

03 "

04 "

05 "

06 "Abillo!"

07 5

08 "Abillo!"

09 5

10 "TST"

11 5

12 **

13 +

14 +

15 RCL 01

16 OCT

17 HMS

18 HMS

19 FRC

20 FACT

21 -

22 +

23 +

24 +

25 +

26 +

27 +

28 LASTX

29 CLD

30 +

31 .END.

GTO .030

30

- Sorry! misspelt!!
You should have
only 7 letters in
your name!

```

11 5
12 **"
13 +
14 +
15 RCL 01
16 OCT
17 HMS
18 HMS
19 FRC
20 FACT
21 -
22 +
23 +
24 +
25 +
26 +
27 +
28 CLX
29 +
30 .END.
      GTO .028
      GTO .029
      30

```

= 77 (byte 119)

Woops! error!

Jump 7

*****LA

02 + } to remove nulls between the jumping
location and -END.

PACKING

GTO .025

LIST 010

25 +

26 +

27 +

28 +

29 +

30 CLX

31 +

32 .END.

GTO .030

GTO .031

30

Woops! Error again!

Display in X

- Print of X! note
lower half of hex
table bytes not
printed and
08 was printed
after CC!



11 + } remove those nulls!
12 +

PACKING

```

GTO .028
LIST 010

```

28 +

29 +

30 +

31 +

32 CLX

33 +

34 .END.

28 CLX - to jump 7 bytes

PACKING

PRP "

01*LBL "TST"

02 5555

03 "

04 "

05 "

06 "Abillo!"

07 5

08 "Abillo!"

09 5

10 "TST"

-END.
is CC|08|29
Byte: 1, 2, 3

) moved two bytes toward
-END.
next page!

Now I make the NNN

~~CΦ/Φ1/29/00/00/00/00~~

192 1 4¹
to go at the top of file 1 - as .END.
which is one reg from R00 - chain intact.

'a packed' .END.

GTO .033
Σ0
BLA)***
RCL I
XEQ "AN"
-0.<<0829000 ***

40<<082900000000
+ ec|08/29 GTO "TST" Back to last file

GTO .034

RCL b

XEQ "AN"

0.00000003-88 ***

0000000003112 Byte 3 of reg (hex) 112
= location of next +

GTO "TST"

GTO .002

RCL b

XEQ "AN"

0.00000002-80 ***

0000000000211: ← Byte 2 of 11A
location of 1st byte of LBL "TST"

GTO "TST"

GTO .003

RCL b

XEQ "AN"

0.00000001-80 ***

0000000000111: ← Byte 1 of 11A

GTO "TST"

RCL b

XEQ "AN"

0.000000002-95 ***

0000000000211: Byte 2 of 11B (END before LBL TST)

GTO "KA"

GTO .000

01 +

02 +

03 +

04 +

05 +

06 +

07 +

PACK

PACK

GTO .001

LIST 014

01 +

02 +

03 +

04 +

05 +

06 +

07 +

08+LBL "KA"

09 "PRS?"

10 PROMPT

11 E

12 -

13 E3

14 /

Top of KA pgm.
in 1st pgm
file.

XEQ "NN" 13.00000000 RUN) error?
XEQ "NN" 192.00000000 RUN
1.0000000000 RUN
41.00000000 RUN
0.0000000000 RUN
0.0000000000 RUN
0.0000000000 RUN

GTO "KA"
GTO .004
RCL b
XEQ "b2"

Y=NNN, X=Rb?
PACK

CAT 1
GTO "KA"
GTO .001
PRP ""

01 .END. ← this is the .END.
GTO "KA"
PRP "" made & put in
just now.

01 .END. ← top 7 bytes
GTO "KA"
LIST 005 still locked up

Notice line number!

02+LBL "KA"
03 "PRS?"
04 PROMPT
05 E
06 -

CAT 1

PACK

GTO "KA"
GTO .001
GTO "KA"
RCL b

0.00000000-44 ***

ENTER

CLX

0.00000000 ***

XEQ "b2"

01+LBL "b2"

TONE 0

TONE 9

"Y=NNN, X=Rb?"

PROMPT

Y=NNN, X=Rb?
NONEXISTENT
memory lost here!
RUN

lock out of keyboard
here - battery
out, in and ---

9

Load in KA ad b2
once more + RCL b
ad byte jumper from cards.

SIZE 020
GTO ..

PACKING
CAT 1

LBL'KA
LBL'KC
LBL'NN
END 287 BYTES

LBL'b2
.END. 98 BYTES

PACKING
PACK
CAT 1

LBL'KA
LBL'KC
LBL'NN
END 287 BYTES

LBL'b2
.END. 98 BYTES

GTO "KA"
GTO .000
01 +
02 +
03 +
04 +
05 +
06 +
07 +

PGM
at top of memory

PACK
XEQ "NN"

159+LBL "NN"
CLA
ASTO 00

162+LBL 02
192.0000 RUN

1.0000 RUN
45.0000 RUN

RUN
OUT OF RANGE

XEQ "NN"
192.0000 RUN -cΦ 1st byte
1.0000 RUN -Φ 2nd byte
45.0000 RUN 29 3rd byte } END
0.0000 RUN } long from
0.0000 RUN } R00
0.0000 RUN } 4 nulls
FIX 9 }
0.0000000000 RUN
-0.012=00000 *** Point of NNN

GTO "KA"
GTO .004
1/X
LASTX
RCL b
XEQ "b2"

Y=NNN, X=Rb?
RUN
PACK
NNN stored in 1st
register of file
just below R00
attempt to clear pgm memory

CAT 1
CAT 1

LBL'KA
LBL'KC
LBL'NN
END 294 BYTES
LBL'b2
.END. 98 BYTES

GTO "KA"
PRP "KA"

.END. ← newer line in END.
GTO .000
PACK

PACKING
PRP ..

.END. ← GTO "KA"
LIST 004

02+LBL "KA"
"PRS?" PROMPT E

GTO "KA"
GTO .000

RCL b
ENTER↑
CLX

XEQ "b2"
Y=NNN, X=Rb?

NONEXISTENT
Lockout - bad out/in
mem lost.

RUN

Digit work
again!

10

note line no. : END.
is line '001'!

Same result
again! Not
sure why or
where!

GTO ..

PACKING
PACKING
TRY AGAIN

SIZE 020
FIX 9
GTO ..

PACKING

GTO "KA"
GTO .000

01 +
02 +
03 +
04 +
05 +
06 +
07 +

PACK

KA and b2
put again
etc.

XEQ "NN"

192.00000000 RUN
1.0000000000 RUN

32.00000000 RUN
0.0000000000 RUN

0.0000000000 RUN
0.0000000000 ***

0.0000000000 RUN
0.0000000000 ***

0.0000000000 RUN
0.0000000000 ***

-0.0120000000 ***

cΦ
Φ 1
zΦ

11

Start again

XEQ "NN" *Start again*
 192.00000000 RUN
 0.000000000+<0 ***
 1.000000000 RUN
 00.0000000< ***
 32.00000000 RUN
 0.000001200-80 ***
 0.000000000 RUN
 0.0000<0012 ***
 0.000000000 RUN
 XEQ "NN"
 32.00000000 RUN
 XEQ "NN"
 192.00000000 RUN
 1.000000000 RUN
 32.00000000 RUN *cph*, .END. after
 0.000000000 RUN
 0.000000000 RUN
 0.000000000 RUN
 0.000000000 RUN
 GTO "KA"
 GTO .005
 RCL b
 XEQ "b2"
 RUN
 PACK
 '=NNN, X=Rb?
ad agin
(way muddle - thought
was wrong - but ok)
cph, .END. after
2ph a GTO ..
 Pack.
 (acc.to Close)
 Store .END.
 at top of file 1
 — How far??

PACKING	CAT 1
LBL ¹ KA	
LBL ¹ KC	
LBL ¹ NN	
END	294 BYTES
LBL ¹ b2	
END	93 BYTES
.END.	85 BYTES

No. 1

Try again.

No. 1

```
LBL "KA"
LBL "KC"
LBL "NN"
END      294 BYTES
LBL "b2"
END      93 BYTES
.END.    05 BYTES
          GTO "KA"
          GTO .000
```

Put in before the .END.

PACKING	PACK
PRIVATE	Pgmat
NONEEXISTENT	GTO "KA"
END	CAT 1
PRIVAT	14 BYTES

PACK
PGM at both went PRIVATE!
but that Pack
cleared all pgm
except for 7 +
→ private
PGM

GTO ..
 PACKING
 END CAT 1
 .END. 10 BYTES ————— note the .END.
 put in : is now a
 (pseudo?) private
 END CAT 1 ← END, not
 a .END.
 PRIVATE
 PRIVATE
 PRIVATE
 PRIVATE RUN
 PRIVATE RUN
 PRIVATE
 END CAT 1
 10 BYTES
 PRIVATE
 PRIVATE
 PRIVATE
 *id@ Σ0 Trying anything!
 GTO .. print
 PACKING

PACKING CAT 1
END 10 BYTES
PRIVATE
CAT 1
END 10 BYTES
.END. 04 BYTES
16 9 RegonSIZE 020
master Clear - KRA-1 b2
in a g.a.n.

```
SIZE 020  
GTO ..  
  
PACKING  
  
XEQ "NN"  
  
152LBL "NN"  
CLA  
0STD 00
```

155*LBL 02
 192.0000 RUN
 1.0000 RUN
 32.0000 RUN } make N N N
 GTO "KA"
 GTO .000

01 +
02 +
03 +
04 +
05 +
06 +
07 +

PACK

Storage location at top
of file 1

GTO .005
RCL b
XEQ "b2"
Y=NNN, X=Rb?
RUN
GTO "KA"
LIST 003

NNN stored.

02+LBL "KA"
03 "PRS?"
04 PROMPT
GTO .000
LIST 005
.END. REG 115 →

01 .END.
PRP ""

result as before

01 +
PACKING
.END.
01 .END.
CAT 1
CAT 1
.END. 07 BYTES
.END. REG 170

put in above the
newly inserted
.END

Pgns all cleared
after the PACK.

PACKING
GTO ..
01 +
02 +
03 +
04 +
05 +
06 +
07 +
PACKING
GTO .005
XEQ "NN"
at top of memory.

Repeat-

PACKING
159+LBL "NN"
CLA
ASTO 00 }
162+LBL 02
STOP
192.0000 RUN
1.0000 RUN
32.0000 RUN
FIX 9
GTO "KA"
GTO .005
RCL b
XEQ "b2"
Y=NNN, X=Rb?
RUN
PACK

Start NNN

Remove PGM?

3

CAT 1
LBL "KA"
LBL "KC"
LBL "NN"
END
LBL "b2"
.END.
294 BYTES
98 BYTES
GTO "KA"
GTO .001
01 +
PACKING
.END.
07 BYTES
GTO ..
PACKING
GTO "KA"
XEQ "NN"

No?

at top
of pgm memory.
key in byte

Pgns gone.
read in again.

152+LBL "NN"
CLA
ASTO 00)
192.00000000 RUN
1.0000000000 RUN
45.0000000000 RUN
0.0000000000 RUN
0.0000000000 RUN
0.0000000000 RUN
0.0000000000 RUN
0.0000000000 ***
0.0000000000 RUN
GTO "KA"
GTO .002
RCL b
XEQ "b2"
Y=NNN, X=Rb?
RUN
CAT 1
GTO .000
01 +
PACKING
PRP ""

cφ/cφ/1/2D/cφ/cφ
unhexed
.END.

loaded into PGM.

11 BYTES
END
PACKING
PACK

been lost here.